

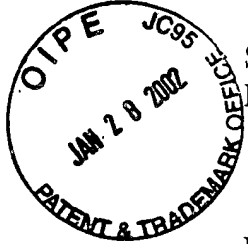
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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re: Attorney Docket No. 1009.004CIP

In re application of: Mark R. Allen



Serial No.: 09/339,616
Filed: June 24, 1999

Group Art Unit: 2821
Examiner: Tuyet Vo
Phone No.: (703) 306-5497
Fax No.: (703) 308-7722

For: Preferred Embodiment to LED Light String

DECLARATION OF DR. MARK R. ALLEN, Ph.D.
SUBMITTED PURSUANT TO 37 C.F.R. §1.132

I, Mark R. Allen, hereby declare:

Introduction

1. I am the inventor named in the above-identified patent application. I have read and understand the Office Action dated August 29, 2000 and all of the references cited therein. The purpose of this Declaration is to traverse the rejections based on Yamuro (U.S. Patent No. 5,941,626) by (1) proving that one of skill in the art would conclude that Yamuro requires a resistor, particularly in view of the prior art; (2) demonstrating, via experiment, that the circuit disclosed by Yamuro, without the resistor, does not work; and (3) explaining the theory behind my invention which distinguishes it from prior art.

Background of Declarant

2. I have a Doctorate degree in Electrical Engineering from the Moore School of Engineering at the University of Pennsylvania (1988). Since then, I have been a high-level

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research engineer in the fields of electrical engineering and electronics, including extensive research of LED technology. Currently, I hold the position of Chief Technical Officer for Fiber Optic Designs, Inc., the assignee of the above-identified application.

All Known Prior Art LED Circuits Powered by AC Require Power Conditioning Circuitry

3. Based on my educational background, extensive research in LED technology and professional experience, I know with certainty that, prior to my invention, power conditioning circuitry was thought to be *required* in an LED circuit powered by an AC source. *All* engineering literature that I have reviewed and studied, prior to my invention, *mandates* the use of current-limiting circuitry for diodes, usually in the form of resistance. Whenever conjectured as a possibility in the art, omission of current-limiting circuitry is *always taught to be unstable, and never used*.

4. The prior art exemplifies that current limiting circuitry, such as a resistor, inductor or capacitor, was thought to be required prior to my invention: Raymond (U.S. Pat. No. 5,936, 599) at col. 2, ll. 50-52 states that “a current limiting resistor 28 (a generating resistor) *must be* connected”, and at line 57 that “the resistor is the *dominant* factor in determining the LED current.” Page 3 of states that “An LED . . . *requires* some kind of current limiting”. Pages 210 and 211 of Light Emitting Diodes--An Introduction, states that “some means for current limitation *has to be* provided”. Pages 215 and 216 of Luminescence and the Light Emitting Diode states that “they [LEDs] *need* a series resistance to limit the current.” Pages 2.18 and 2.19 of Hewlett Packard’s Optoelectronics Manual, show that one of ordinary skill in the art would think that Applicant’s invention was *not* obvious since removal of current limiting circuitry is taught to harm the LED lamp. These references are examples of engineering literature relied on

by those skilled in the art to build LED circuits. Copies of these references are included as **Exhibit A.**

5. Furthermore, leading LED manufacturers throughout the world, such as Everlight, J & K Super Bright Industrial, Ltd., Yiow Shie Industrial Co., Ltd. and Lite-On Electronics Inc., teach that a resistor *must* be put in series with a power source to (1) protect the LED and (2) provide a stable circuit.

6. Everlight was established in 1983 to produce LED Lamps. The complete history of this highly successful LED manufacturer is provided in their 1999 catalog, pertinent portions of which are attached hereto as **Exhibit B.** In 1999, the company had over \$130 million dollars (U.S.) in sales. Its management and researchers are “skilled in the art” of LED technology. On page 114 of Everlight’s 1999 catalog they clearly teach the use of a protective resistor is necessary and must be used.

7. J &K Super Bright Industrial, Ltd. was established in 1986. It has achieved ISO 9002 certification, employs about 1,000 workers, and claims an R&D Division that keeps researching for new technologies and materials. This company also offers assembly aimed at custom design. Its management and researchers are “skilled in the art” of LED technology. On the page entitled “How to Use LEDs” under the section “Preventing Overcurrent”, J&K teaches to “put protective resistor in series, not only to prevent overcurrent, but to keep LED in uniform brightness.” **Exhibit C.**

8. Yiow Chie Industrial Co., Ltd. was established in 1986, claiming “modern equipment and the most recent R&D.” Its management and researchers are “skilled in the art” of

LED technology. Page 83 of its catalog clearly shows the requirement of a resistor for preventing overcurrent. **Exhibit D.**

9. Lite-On Electronics, Inc. is one of the world's largest independent manufacturers of optoelectronic products, including LEDs. Lite-On's 1998-99 Catalog claims that it offers a wide range of reliable and economical opto-electronic design solutions, and is the first IEQC - certified opto products manufacturer in the world, also qualified for ISO 9000 & ISO 1400 certification. Its management and researchers are "skilled in the art" of LED technology. The instructions for "How to Use LITE-ON LED Lamps" clearly teach to "put protective resistors in series" and that the "circuit must be designed so that overvoltage (overcurrent) is not applied to the LED during ON/OFF switching." **Exhibit E.**

Yamuro's Teachings are Consistent with Other Prior Art References Requiring a Resistor

10. Being one of skill in the art, it is my opinion that Yamuro (U.S. Patent No. 5,941,626) clearly teaches that a resistor is required. Yamuro states:

Since the required power source of 100V is equal to the common source voltage in Japan, the resistance 8 apparently seems unnecessary. However, it is proved from experience that the apparatus is stable in function *by providing the resistance 8. Therefore, the resistance 8 is connected* to the circuit shown in FIGS..." (italics added for emphasis).

Yamuro then goes to include, throughout his patent, text and diagrams incorporating a current-limiting resistor in the circuitry without any further reference to any idea or possibility of its omission.

The above *direct quotation* from Yamuro, which is the *only reference made* in his entire patent to the *possibility* of removing his current-limiting resistor, is one that *does not, in any way, advocate or even allow* removal of the resistor. *Exactly the contrary* is taught by Yamuro; that is,

that the *resistance makes the circuit stable, and therefore it is to be used*. Yamuro uses the resistor throughout his patent in the figures and description, which is consistent with the usual design convention of LED circuitry, prior to my invention. Yamuro states the design “...is stable...” by including the resistor. Yamuro does *not* state “...is more stable...” Yamuro directly implies that the circuit is unstable without the resistor. Therefor, the teachings of Yamuro would certainly *always* lead one of skill in the art to include the resistor.

11. Disinterested parties of ordinary skill in the art also would not interpret Yamuro to teach a desire to do away with the resistor. Attached as **Exhibit F** is a letter from Mr. Duane J. Knize, Chief Scientist for the Technology Research Group at Science Applications International Corporation (SAIC). Mr. Knize has over 27 years of engineering experience including circuit and electronic system design and development. In the second paragraph of his letter, Mr. Knize states that “the discussion in column 3 lines 30-40 clearly promulgates a requirement for this resistor” and later in the letter that “it seems clear to me that the author of patent 5,941,626 considered the resistor to be an essential part of his invention.”

12. Furthermore, two authors and editors of a number of technical journals, including the editor-in-chief of the *AT&T Technical Journal* for Bell Laboratories, have concluded that lines 30-40 of Yamuro, read *together* clearly teach that a resistor is required. One of these authors and editors, Mr. James M. Murray, President of Timberock USA Company, concluded that the statement in lines 30-33 that “the resistance 8 apparently seems unnecessary” is qualified by and immediately disproved by the statement in lines 34-37 that “[h]owever, it is proved from experience that the apparatus is stable in function by providing the resistance 8. Therefore, the resistance 8 is connected to the circuit shown in FIGS. 1A and 1B”. Attached as **Exhibit G** are true and correct copies of (1) a “Letter of Testimony” from Mr. Murray and (2) a

letter from Mr. Bert Vorchheimer, former Editor-In-Chief of the At&T Technical Journal at Bell Laboratories.

Experimental Evidence Proves that Yamuro Could Not Teach Omitting the Resistor

13. I performed the experiments described herein. The purpose of the experiments was to demonstrate that the Yamuro circuit, without the resistor, does not work. The experiments confirm my understanding of Yamuro; that is, it does not disclose, teach or suggest an optional circuit design that omits the resistor.

14. Paragraph 7 of the Office Action states **“the teaching supported by line 37, column 3 clearly suggests the removal of the resistor(8)”, “[I]f one were to construct figure 1B to be used in Japan, a resistor (8) would have been inherently eliminated as clearly pointed out by this teaching” and “given the power situation in Japan as it is suggested under line 37, column 3, one could not help but to construct the circuit without the resistor (8).”** I respectfully disagree.

15. I have constructed three LED circuits, according to the specifications listed in column 3, line 31-43 of Yamuro, to demonstrate that the Yamuro reference could not be read to suggest the removal of the resistor. A fourth LED circuit was constructed using 2.2VDC LEDs.

16. The experiments show that removing the resistor causes the circuit taught by Yamuro to fail. The experiments were videotaped to show the circuit failures. Three copies of the video tape as well as a transcribed copy of the tape is being submitted as **Exhibit H**.

17. Since the Examiner reads Yamuro to suggest that the resistor is optional, the circuit configurations that omit the resistor will be referred to as the “Examiner Option” and the circuits including a resistor will be referred to as the “Yamuro Circuit” or “Yamuro Design”.

18. Each circuit was constructed using 2-volt DC LEDs, as specified by Yamuro at column 3, line 31. It is well known in the industry that LED voltage specifications are calculated under DC operating conditions. Thus, I will refer to the LEDs disclosed by Yamuro, as well as those used in the experiments as “2-volt DC” LEDs.

19. The specific LED’s used were manufactured by Ledtech, Inc. and bear part number LT1833(4)-81-M1. The LEDs produce a 20 mA nominal DC current. Attached as **Exhibit I** are the specification sheets for these LEDs.

20. Each light string circuit will be referred to as a separate “case”. Since I did not have access to a 100VAC source, as called for in Yamuro, the circuit of cases 1 and 3 assume an AC source voltage of 110VAC, and the circuit of cases 2 and 4 assume an AC source voltage of 120VAC. Typical U.S. household source voltage varies between 110VAC and 120VAC. Thus, experiments were conducted assuming both conditions to ensure the validity of the conclusions drawn from the experiments.

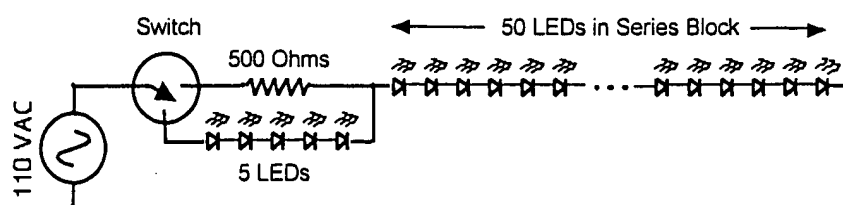
21. To compensate for using source voltages that are 10 VAC and 20VAC above the 100VAC called for by Yamuro, five (5) 2-volt DC LEDs were added to cases 1 and 3, and ten (10) 2-volt DC LEDs were added to case 2. To demonstrate both the Examiner’s Option (resistor optional) and Yamuro’s Design (resistor required), the circuit in cases 1 and 2 is connected through a switch to either a resistor or a number of LEDs. In one position, the resistor completes

the circuit according to my understanding of Yamuro (Yamuro Design). In a second position, LED's are substituted for the resistor, as suggested by the Examiner (Examiner's Option).

22. The value of the resistor in all cases is calculated according to one of the examples provided in Yamuro at column 3, lines 40-43, which states "50 or less LED lamps 4, for example 45 or 40 LED lamps, can be connected to the light emitting unit 6. In this case, the resistance value corresponding to the potential difference from the power source 9 is set as the resistance 8." I selected Yamuro's 45 LED example for cases 1 and 2, then added five (5) 2-volt DC LEDs to the circuit of case 1 and ten (10) 2-volt DC LEDs to the circuit of case two, to equate the 100VAC source voltage in Japan to the assumed 110VAC and 120VAC source voltages in the U.S., respectively.

Experiment Case #1

23. For case 1, fifty (50) 2-volt DC LEDs were used, which accounts for 100VAC of the assumed 110VAC source, leaving 10VAC to be compensated by either (1) a resistor when the switch is in the first position, or (2) a number of LEDs when the switch is in the second position. In the first position, a resistor value of 500 Ω was calculated by dividing 10VAC by the 20 mA specified nominal current produced by the LEDs. In the second position, the number of LEDs was calculated by dividing 10VAC by 2-volts (the specified LED voltage) yielding five (5) LEDs. The circuit of case 1 is schematically represented as follows:



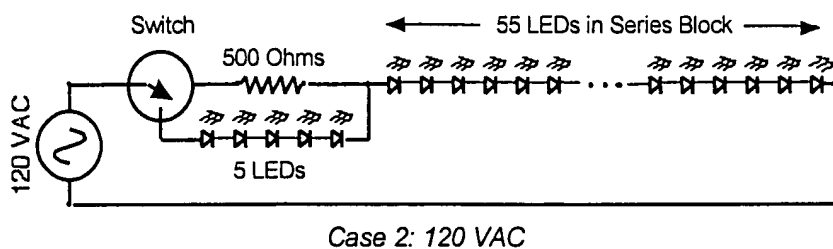
Case 1: 110 VAC

24. The experiment of case 1 was conducted by placing the switch in the first position, then plugging the circuit into a household receptacle. This configuration mirrors the Yamuro Design. The circuit was stable and the resistor became quite hot. I agree that Yamuro, as well as a number of other prior art references made of record in this case, teach this circuit (i.e. Hewlett Packard's Operational Considerations for LED Lamps and Display Devices notes that a resistor must be used to maintain circuit stability). *See Exhibit J.* The switch was then moved from the first position to the second position, replacing the resistor with an equivalent number of LEDs. This circuit represents the Examiner's Option. This circuit considers the Examiner's statement that Yamuro "clearly suggests the removal of the resistor 8". It is an example of the circuit that the Examiner contends is "operatively stable" in paragraph 7 of the Office Action (except of course for the five (5) LEDs added to this experiment to account for the difference in source voltage). When the switch was moved to the second position, the LEDs immediately dimmed, indicating that the circuit was being stressed. The LEDs then quickly began to fail until the entire LED string failed.

25. Based on the observed results of the experiment of case 1, I conclude that the Yamuro circuit without the resistor is inoperable. Therefore, Yamuro could not be read to suggest the removal of the resistor.

Experiment Case #2

26. For case 2, fifty-five (55) two (2) volt DC LEDs were used, which accounts for 110VAC of the assumed 120VAC source, leaving 10VAC to be compensated by either (1) the resistor when the switch is in the first position, or (2) a number of LEDs when the switch is in the second position. Similar to calculations performed for case 1, the value of the resistor connected to one side of the switch was 500 Ω and five (5) LEDs were connected to the other side of the switch to account for the 10VAC. The circuit of case 2 is schematically represented as follows:



27. The experiment of case 2 was conducted in the same fashion as case 1 with nearly identical results. With the switch in the first position, the circuit was stable and the resistor was very hot. With the switch in the second position, the lights immediately dimmed, indicating that the circuit was being stressed; then the LEDs slowly began to fail until the remaining LEDs all failed virtually simultaneously. The only difference between case 1 and 2 was that the circuit of case 2 took slightly longer to fail when the resistor was removed, but the circuit was nonetheless highly unstable.

28. Based on the observed results of the experiment of case 2, I conclude that the Yamuro circuit without the resistor is inoperable. Therefore, Yamuro could not be read to suggest the removal of the resistor.

Experiment Case #3

29. The circuit of case 3 was similar to case 2, however the switch and additional LEDs were omitted from the circuit. Fifty-five (55) 2-volt DC LEDs were connected in series to a 500 Ω resistor. Assuming an AC source voltage of 110 VAC, the circuit was stable and performed as expected, **due to the resistor**.

30. The resistor was then physically removed from the circuit to test the Examiner's assertion in paragraph 7 of the Office Action that "the teaching supported by line 37, column 3 clearly suggests the removal of the resistor(8)". The circuit, when connected to the AC source, failed immediately. In fact, in the video demonstration of this particular experiment, one can hear the LEDs "pop" when the circuit is connected to the AC source.

31. Based on the observed results of the experiment of case 3, I conclude that the Yamuro circuit without the resistor is inoperable. Therefore, Yamuro could not be read to suggest the removal of the resistor.

Experiment Case #4

32. The circuit of case 4 was identical to the circuit of case three, except 2.2-volt DC LEDs were used. Fifty-five (55) 2.2-volt DC LEDs were connected in series to a 500 Ω resistor. This circuit duplicates the example in Yamuro where 50 2-volt DC LEDs + a resistor are used at 100VAC. As expected, **due to the presence of the resistor in the circuit**, the circuit was stable. The resistor was then removed from the circuit leaving fifty-five (55) 2.2-volt DC LEDs connected in series to the electrical plug. According to the Examiner's reading of Yamuro, that an LED circuit can be stable without the resistor if sum of the specified LED DC voltage for each

LED in the circuit matches the AC source value, this circuit should be stable up to 121VAC (2.2-volts x 55 LEDS). However, the circuit failed almost immediately when connected to the AC source.

33. Based on the observed results of the experiment of case 4, I conclude that the Yamuro circuit without the resistor is inoperable. Therefore, Yamuro could not be read to suggest the removal of the resistor.

Summary of Experimental Results

34. A chart summarizing the results of the experiments is provided below:

Case	Voltage	Resistor	LEDs	Description	Result
1	110 VAC	500 Ω	50 2-volt DC	Yamuro Design	<i>Stable, Works</i>
1	110 VAC	None	55 2-volt DC	Examiner "Option"	<i>Fails Quickly</i>
2	120 VAC	500 Ω	55 2-volt DC	Yamuro Design	<i>Stable, Works</i>
2	120 VAC	None	60 2-volt DC	Examiner "Option"	<i>Fails Quickly</i>
3	110 VAC	500 Ω	55 2-volt DC	Yamuro Design	<i>Stable, Works</i>
3	110 VAC	None	55 2-volt DC	Examiner "Option"	<i>Fails Quickly</i>
4	120 VAC	500 Ω	55 2.2-volt DC	Yamuro Design	<i>Stable, Works</i>
4	120 VAC	None	55 2.2-volt DC	Examiner "Option"	<i>Fails Quickly</i>

35. From these experiments and several others I conducted using LEDs from different manufacturers yielding the same results (which are not set forth herein), I conclude that any LED circuit that is designed by matching the sum of the DC specified voltages of all of the LEDs in

the circuit to the AC source voltage will be unstable and fail. Accordingly, the mere substitution of LEDs for a resistor using LED voltage specifications which are always specified under *DC operating conditions* leads to circuit instability and inevitable circuit failure.

The Theory Behind Applicant's Invention that Distinguishes it from the Prior Art

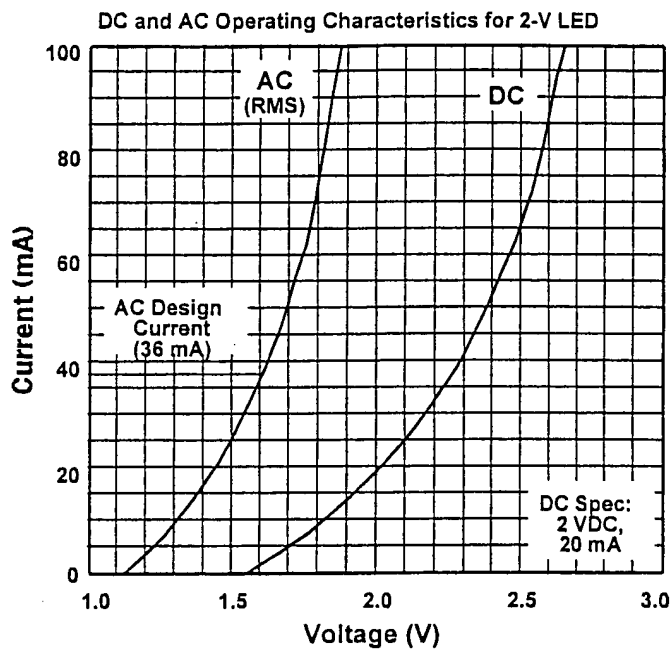
36. LED circuit theory is based on DC power and the fact that LEDs, being diodes, have a highly nonlinear current-versus-voltage characteristic curve; unlike a resistor whose current-versus-voltage characteristic curve is linear. LEDs are "current driven" devices which fail if the applied DC voltage is too high. As such, the prior art teaches that all LED circuits *require* an auxiliary circuit that limits input current as voltage is increased. Limiting the input current as the voltage increases stabilizes the LED circuit by *linearizing* its current-versus-voltage characteristic curve. The simplest auxiliary circuit is a single impedance element. For DC power, the impedance element is a resistor.

37. It is known that LED circuits can be powered by pulsed DC or AC power, in addition to steady-state DC power. However, these different sources of power do not change the fundamental fact that diodes are current-limited devices. Thus, even the most recent prior art teaches that LED circuits *require* at least one impedance element. In the case of AC power, the impedance element may be a resistor, or a reactive element such as a capacitor or inductor. Since the reactance of a capacitor or inductor is constant at a steady frequency (such as 60Hz), the reactive element behaves like a resistor and therefor, can be substituted for a resistor. US Patent 5,936,599 to Raymond shows such substitution.

38. To arrive at my invention, I challenged the assumptions that stem from traditional LED circuit theory mentioned above, including the assumption that an impedance element is required to stabilize an LED circuit driven by AC power. Knowing that LED performance

specifications are provided under DC operating conditions, I discovered that the DC specifications can not be used to gauge the performance of LEDs *under stable AC operating conditions* because of the nonlinear current-versus-voltage characteristic of diodes. With AC input, the LEDs are off over half the time while the voltage is below some positive threshold value (reverse bias). Then, as the voltage increases beyond RMS to peak value and falls back again, diode current varies accordingly in a nonlinear fashion. I discovered that, for AC power, the average (RMS) LED current is much larger than that which would be obtained if the LED were powered by a DC waveform whose voltage is equal to the average (RMS) voltage of the AC waveform. In other words, if the *DC specified* LED voltages are used to match the AC source voltage in the LED circuit, the resulting LED current will be much larger than desired, and the circuit will be unstable and fail. The experiments I performed (described above) fully support this theory.

39. To arrive at the claimed invention, I measured LED current at varying *AC voltages* for a particular LED to create an *AC operating curve* (i.e. I-V characteristic curve) for the LED. The graph below is an example of a *DC operating curve* for a 2-volt DC LED (typically provided by the manufacturer) and the corresponding *AC operating curve* for the same LED:

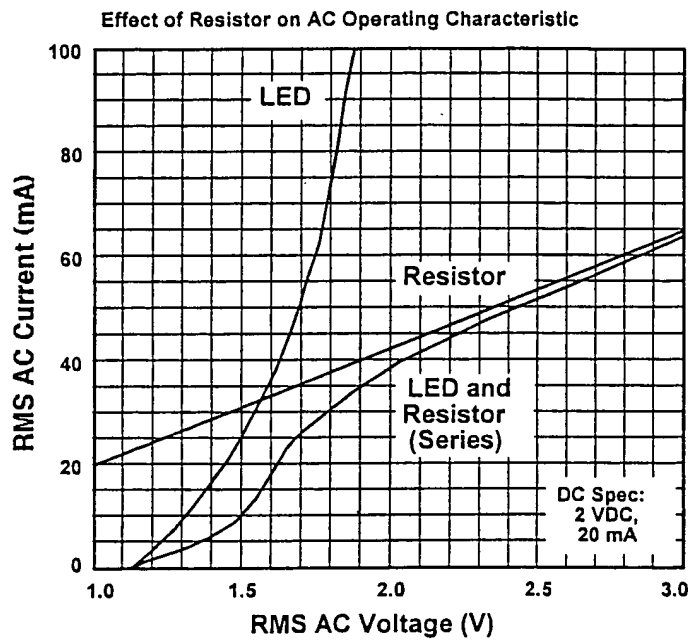


As you can see from the graph, 2-volts on the horizontal axis does not cross the AC operating curve below 100mA, which is known to be the typical maximum operating current (I_{max}) of an LED. This observation is significant and explains the failures in the experiments performed above. That is, the current produced by matching 55 2-volt DC LEDs to a 110 VAC source exceeds the maximum operating current of the 55 LEDs, thereby causing the LEDs to fail.

40. According to my invention, the *AC voltage* of the LEDs are matched to the AC source. Thus, the current produced by the LEDs falls within a range that is acceptable and stable. For example, from the curve, any value of current between the turn-on voltage (or threshold voltage) of the LED and the maximum voltage (or voltage at which the LED fails) can be

selected as the *AC operating current* and the corresponding *AC operating voltage* of the LED can be read. If the AC source voltage of an LED circuit is 110VAC, fifty-five (55) 2-volt AC LEDs can be connected in series to the source and be stable.

41. The effect of a resistor on this AC operating curve is shown below:



I created this curve to show how the resistor linearizes the AC current-versus-voltage characteristic curve of an LED. However, I discovered that the resistor could be eliminated by reading the AC curve *directly* and selecting an operating voltage at a stable point on the AC curve. For example, according to the example provided, a 2-volt DC LED is stable at 1.6 VAC. Thus, an LED circuit powered by a 110 VAC source would be stable if it comprised 69 of these 1.6 VAC LEDs.

42. A brief videotaped presentation detailing the theoretical distinction between *AC characterized* and *DC characterized* LEDs follows the experiments on the videotape submitted as Exhibit H.

43. I inspected a set of light strings displayed by Excellence Optoelectronics, Inc. at an electronic show in Taipei from October 9-12, 2000. The light set was identical to my a light string according to my invention and built according to the teachings of my application for patent.

I declare under penalty of perjury that the foregoing is true and correct.

Executed on Feb 27, 2001.

Mark R. Allen, PhD

Mark R. Allen

Chief Technical Officer

Fiber Optic Designs, Inc.

LIGHT EMITTING DIODES

AN INTRODUCTION

Klaus Gillessen
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Prentice/Hall  International

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Singapore Sydney Tokyo Toronto

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7 APPLICATIONS

7-1 General aspects

Because light emitting diodes are solid state, semiconductor light sources, they have some specific properties in common with other semiconductor devices like transistors or integrated circuits. Some general features of LEDs and other semiconductor devices are small size, low weight, high mechanical stability, low temperature sensitivity, high reliability, long operating lifetime, and last but not least, low price. As electrical devices, LEDs are characterized by low operating voltage, medium current, and high speed. From an optical point of view, the most important properties of LEDs can be summarized as follows: LEDs are active emitters of nearly monochromatic light with highly saturated colors.

From this set of properties some specific fields of application can be derived, which are treated in detail in the following parts of this chapter. Only a few general remarks will be made here. LEDs are mostly used at the periphery of electronic equipment of all kind. Interfacing LEDs to electronics is especially easy, because their driving requirements can be easily fulfilled by standard transistors and integrated circuits. This property is decisive for the bulk of LED applications. Typical examples are status indicators, displays for entertainment electronic equipment, and displays for measuring equipment. LEDs are also preferred to other display tech-

nologies in rough environments and where high reliability is imperative.

On the other hand, the properties of LEDs define also the limits of their applicability. For example, LEDs are not well suited for general illumination purposes, because their brightness is still inferior to other light sources, and because white light can hardly be achieved (blue LEDs are less efficient than red, yellow and green devices), but is required for color-neutral illumination. LEDs are also rarely used in battery operated equipment, where power consumption is a critical factor. Here liquid crystal displays (LCD) are dominating. The relatively high power consumption, and consequently, power dissipation of LEDs renders also the realization of high resolution, flat LED screens more difficult (see section 7-3-4). For color picture displaying, the inferior brightness of blue emitters is another limitation. Other competing flat screen technologies like LCDs or plasma displays also have difficulty in displacing the cathode ray tube, which is well developed. It offers rather high performance at quite low cost.

7-2 Driving of LEDs

7-2-1 Current limiting

The I-V characteristics of LEDs are those of normal pn diodes: in the forward direction, which is the normal operating mode, the current remains rather small until the voltage amounts to approximately E_g/e , which is about 1.9 V for red LEDs, 2.0 V for orange, 2.1 V for yellow, and 2.2 V for green. Around these voltages the current rises exponentially (see chapter 1). Because the brightness of an LED is determined by the current flowing through the device, it is the

current which has to be defined during operation. If an LED is driven by a voltage source, its brightness is very sensitive to small voltage fluctuations, due to the steep I-V characteristics. Therefore, some means for current limitation has to be provided. Two examples for current definition in LED circuits are shown in Fig. 7-1. The simplest possibility is a resistor in series to the LED (Fig. 7-1, left). Here the current through the LED (I_{LED}) is defined by the intersection of the LED characteristics with the straight line given by (operating voltage minus LED voltage) divided by resistance, which is $(5 \text{ volts} - V_{LED})/150 \Omega$ in the example shown. As can be seen easily, the LED current is now far less sensitive against voltage changes. In most practical applications the value of the resistor can be calculated using the simple formula:

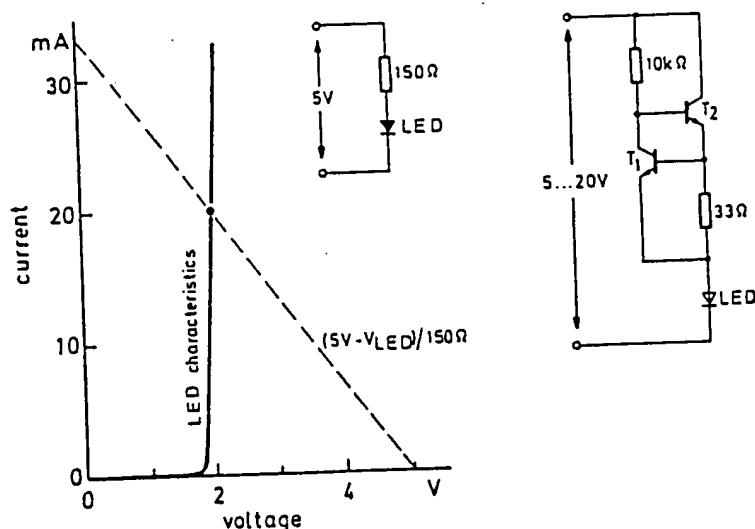


Fig. 7-1 Methods of control of LED current, left: LED with series resistor, determination of working point, right: LED with constant current source for approximately 20 mA.

$$R = (\text{operating voltage} - 2 \text{ V}) / \text{LED current} \quad (7-1)$$

because the forward voltage required for a normal operating current is roughly 2 volts for all types of LEDs.

If large voltage variations are to be expected, LEDs are driven best using a constant current source. For this purpose integrated circuits are available which are supplied by several manufacturers. A simple constant current source can also be realized with two transistors and two resistors as shown on the right hand of Fig. 7-1. This circuit controls the LED current via the voltage drop along the 33Ω resistor, which is 0.66 V at 20 mA output current. If the current increases, the transistor T_1 becomes more conducting, thus diminishing the current flowing into the base of the transistor T_2 . Therefore, the output current is decreased again. The value of the second resistor is determined by the minimum voltage to be expected and the base current necessary to drive T_2 . In the example shown it is assumed that at least 2 V are available at this resistor (5 V minus LED voltage minus collector emitter voltage of T_2), and that 0.2 mA are sufficient to cause an output current of 20 mA, i.e. the current gain of T_2 should be at least 100. The upper voltage limit is given by the power dissipation in T_2 which is roughly 20 V times 20 mA or 0.4 W.

7-2-2 Multiplex operation

Because the light output of an LED is proportional to the operating current over a wide current range, LEDs can be driven by pulses instead of continuous current. The apparent brightness is then given by the average current. Some LED types even exhibit a superlinear current-light relationship, so that a net gain in brightness results in pulsed operation. This property is widely used in time multiplex operation of

Light-emitting diodes

**A.A. BERGH
and
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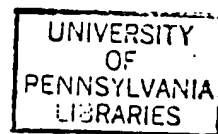
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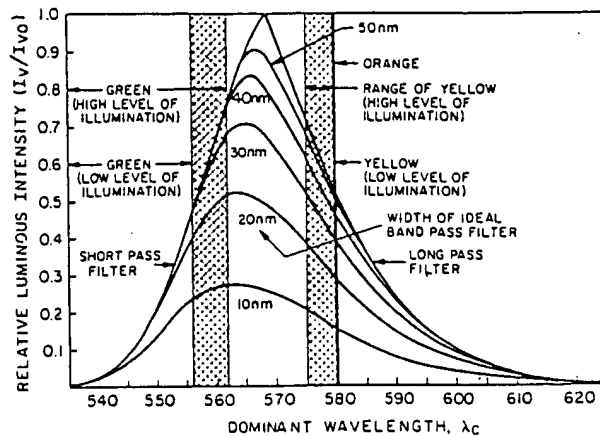


Fig. 7.13 Relative luminous intensity of a yellow-green GaP:N lamp as a function of dominant wavelength, using a series of ideal cut-off filters and a series of narrow band pass filters [9, 10].

can be shifted either to the green or to the yellow-orange. The data in Table 7.3 demonstrate, however, that the over-all lamp performance is not necessarily degraded to the same extent as the brightness of the lamp. With the green colour, for example, both a short pass and a narrow band pass filter yield a performance figure (T^2/T_a') greater than unity, indicating that the loss in brightness is compensated by the increase in contrast. Simultaneously, the colour discrimination in white ambient light also improves.

7.1.3. Basic LED drive circuits

The operation of LEDs is regulated by the forward current-voltage characteristics of semiconductor diodes.

$$I_F = I_0 \exp(qV_F/nk_B T) \quad , \quad (7.9)$$

where the value of n is usually in the range of 1.5 – 2.0. The light emission characteristics of the devices follow a similar expression (except for very low current densities),

$$\Phi \propto \exp(qV_F/nk_B T) \quad , \quad (7.10)$$

with $n = 1$ until the light-emitting centres saturate ($1 - 5 \text{ A cm}^{-2}$ only for red GaP:Zn,O). As a result the light emission is usually superlinear with increasing current, and hence it is advantageous to use low duty cycle pulsed drivers.

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The turn-on voltage is governed by the built-in potential of the device V_{bi} , which in turn depends on the energy gap of the semiconductor and the distance of the Fermi level from the conduction and valence bands

$$qV_{bi} = E_g - (qV_n + qV_p) \quad (7.11)$$

Typical operating voltages are 1.2 V for GaAs, 1.7 V for red $\text{GaAs}_{1-x}\text{P}_x$, and 2.0 V for GaP devices. The temperature-dependence of the forward voltage is similar for all devices, approximately $-2 \text{ mV } ^\circ\text{C}^{-1}$.

LEDs must be biased from a constant current source. In case of a constant voltage source this can be approximated by placing a resistor in series with the power supply, as shown in Fig 7.14. The resistor, usually a silicon chip,

TABLE 7.3
*Calculated lamp performance as a function
of ideal filter parameters
(incandescent light at 2560 °C)*

λ_1 (nm)	λ_2 (nm)	Type of filter	Colour †	λ_c (nm)	T ‡	T'_a §	T^2/T'_a
540	IR	Long pass	Y-G	569	0.993	0.805	1.22
550	IR		Y-G	570	0.945	0.728	1.23
560	IR		Y-G	574	0.751	0.642	0.88
570	IR		Y	582	0.479	0.550	0.42
UV	570	Short pass	G	557	0.521	0.450	0.60
UV	580		G	561	0.745	0.544	1.02
UV	590		G-Y	565	0.878	0.637	1.21
UV	600		Y-G	567	0.944	0.720	1.24
UV	610		Y-G	568	0.975	0.795	1.20
530	560	Band pass	G	551	0.248	0.228	0.27
540	570		G	557	0.514	0.255	1.04
550	580		G-Y	563	0.690	0.273	1.74
560	590		Y-G	570	0.630	0.279	1.42
570	600		Y	578	0.423	0.270	0.66
580	610		Y	587	0.231	0.250	0.21

† Y: yellow; G: green.

‡ T : relative loss of luminous output due to filter.

§ T'_a : double pass luminous transmittance of filter to the ambient illumination.

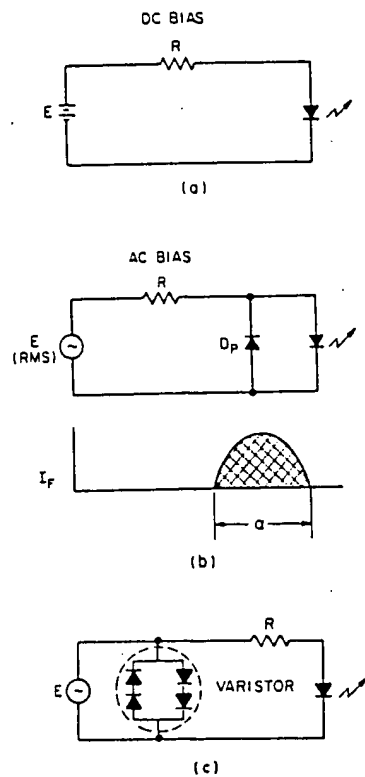


Fig. 7.14 Basic LED drive circuits using a constant voltage source.

can be built into the LED package as shown in Fig 7-6 (c). The value of the desired resistance R is simply given as

$$R = (E - V_F)/I, \quad (7.12)$$

where E represents the applied voltage.

For the a.c. operation the average forward current is

$$I_F = \frac{E\sqrt{2}}{\pi R} \int \left(\cos x - \frac{V_F}{E\sqrt{2}} \right), \quad (7.13)$$

where pulse width α is given by

$$\alpha = \cos^{-1}(V_F/E\sqrt{2}). \quad (7.14)$$

The rectifier diode D_p is optional for LEDs with low reverse breakdown voltages.

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For a one-sided abrupt junction such as a diffused $\text{GaAs}_{1-x}\text{P}_x$ diode, the breakdown voltage is given as

$$V_B = \frac{\epsilon_s \mathcal{E}_m^2}{2q} (N_B)^{-1}, \quad (7.15)$$

where ϵ_s is the semiconductor permittivity, \mathcal{E}_m the maximum field, and N_B the ionized background impurity concentration of the lightly doped side of the junction. If the potential of the voltage supply is such that $E(\text{RMS}) > B_V / \sqrt{2}$ (B_V can be as low as 3 V), excess reverse current will reduce the efficacy of the LED.

The maximum flow of current to the LEDs under variable drive conditions can be limited with varistors as shown in Fig 7.14 (c). Under low current condition, the varistor draws no current. At higher currents, as the voltage across the varistors increases, the fraction of current through the varistor steeply increases. An important feature of this circuit is its lower vulnerability to current surges. Most of the energy from a transient pulse is absorbed by the varistors, which are generally more rugged than the LEDs.

Constant current drive for the typical and most common on or off indicators is provided by saturated transistor switching, as shown in Fig 7.15. If the device is connected in series with the transistor circuit Fig 7.15 (a), the current supplied to the LED when the switch is closed is given as

$$I_F \sim (V_{cc} - V_{SAT} - V_{F,LED})/R. \quad (7.16)$$

For applications where it is desirable to minimize the current variations in the power supply in order to avoid regeneration currents in associated circuits, shunt saturated switching is more desirable (Fig 7.15 (b)). When the switch is open, the current supplied to the LED is

$$I_F \sim (V_{cc} - V_{F,LED})/R. \quad (7.17)$$

Finally, in applications where the light output of the LED must be modulated, the drive transistors operate in the active mode with the LEDs connected in series with the collectors. For a high impedance input such as shown in Fig 7.15(c), the input current of the transistor is given as

$$I_{BE} \approx E/h_{FE}R, \quad (7.18)$$

and the current supplied to the LED is given as

$$I_F = (E - V_{BE})/R. \quad (7.19)$$

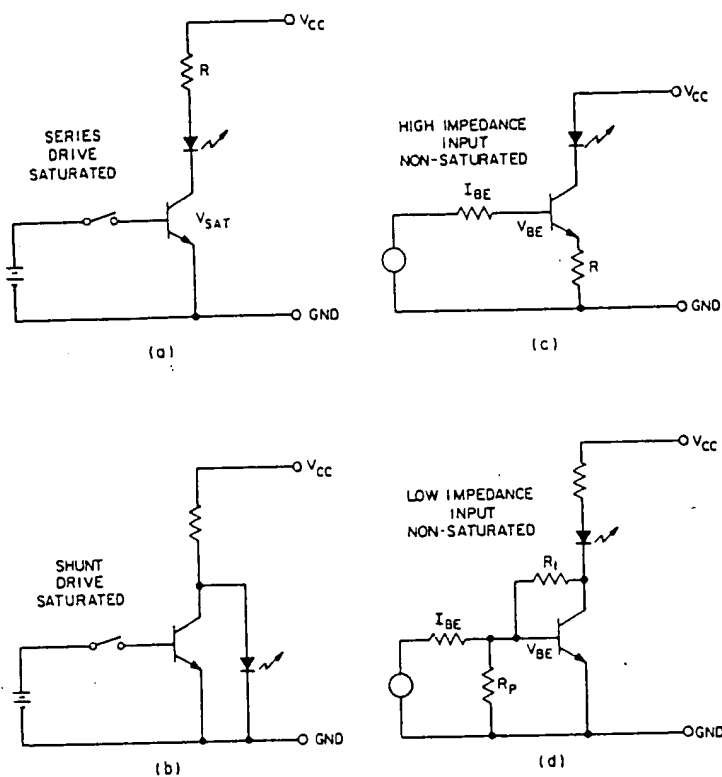


Fig. 7.15 Constant current transistor drive circuits for LEDs.

In case of a low impedance input (Fig 7.15 (d)), the corresponding expression is given as

$$V_{EB} \approx I_{BE} R_p / h_{fe} (R_2 / R_1) \quad (7.20)$$

and

$$I_F \approx (R_1 / R_2) I_{BE}. \quad (7.21)$$

7.2. LED displays

The basic formats for LED displays are shown in Fig 7.16. The seven segments and the 3×5 array are usually used to display numbers from 0 to 9, although they are capable of displaying some upper case letters (A,B,C,D,E,F,G, H,I,J,L,O,S,U) and a few lower case letters (b,c,d,h,i,l,n,o,r,u). For numeric displays the seven-segment format is the most widely used, while for

LUMINESCENCE AND THE LIGHT EMITTING DIODE

*The Basics and Technology of LEDS and the Luminescence
Properties of the Materials*

by

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10 APPLICATION OF LED PRODUCTS

10.1 INTRODUCTION

The term LED (Light Emitting Diode) is somewhat misused in that although it refers to a rectifying junction which emits light, it is often more loosely used to refer to an LED indicator lamp. This product is also known as: a discrete LED, a solid-state lamp, a semiconductor lamp, etc. The reason for this confusion is partly historic in that many research workers use a transistor header for the evaluation of LED die and it was in this housing that the first LED indicator lamps were first sold.

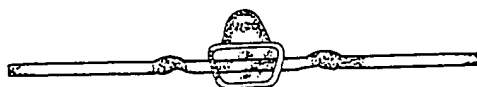
LEDs are used in a wide variety of products, most of which have application in the field of displays. However, it seems that rather than replacing existing products, these new devices tend to be used in new applications. In this chapter we shall divide the LED products into three main headings:

- (a) LED indicator lamps,
- (b) LED displays,
- (c) Optically coupled devices.

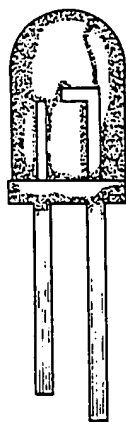
We shall discuss the use of LEDs in these products and point out the range of applications for which these products are or could be used.

10.2 LED INDICATOR LAMP

LED indicator lamps fall primarily into two types of construction as illustrated in Fig. 10.1, i.e. the radial and axial lead constructions. The radial lead type which was one of the first lamps introduced by Monsanto (Ref. 1) owes its popularity to its minute size and low cost. It is mainly suited for mounting on printed circuit boards. The axial lead construction is more versatile in that it is available in a range of diameters and heights and the leads can be wire wrapped. It is also generally supplied with a plastic mounting bush so that it can be used either as a panel-mounted lamp or directly on to a printed circuit board. As discussed previously LED indicator lamps are produced with a variety of plastic encapsulations, and this is important because many panel indicators are sold not only for their technical specifications, but because of aesthetic considerations. The viewing angle for a warning light may in one case be required to be very wide so that it can be seen from a wide range of positions, but in another application, such as on a car dashboard, it may only be required to be seen by the driver.



(a) Radial lead construction



(b) Axial lead construction

Fig. 10.1 Standard types of LED indicator lamps.
(Monsanto "GaAsLITE" catalogue, 1973.)

The electrical characteristic of most LED indicator lamps is normally just that of the rectifying junction, i.e. they need a series resistance to limit the current. However, some manufacturers do incorporate a current-limiting device in the lamp so that it can be operated directly from a battery, and hence compete as a direct replacement to a subminiature filament indicator lamp.

10.2.1 Range of Applications

The smallest LED indicator lamps are normally designed for mounting directly on printed circuit boards where the compatibility with transistors and integrated circuits make them an obvious choice for circuit status indicators and fault indicators to facilitate servicing. If the circuit board is positioned closely behind a suitably designed contrast enhancement filter then the circuit board can be hidden and the lamp will only be apparent when energized. This is the basis of a splash-proof display with a number of possible consumer applications, e.g. TV channel-selector indicator.

A slight modification of this idea is the display of a hidden legend. As illustrated in Fig. 10.2 this is the illumination of a translucent letter or sign by an LED which has a specially designed radial distribution to give a uniform surface illumination.

Optoelectronics Applications Manual

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curve is normally given on all data sheets. As a final example assume a lamp is strobed at 20 mA peak current at a 10% duty cycle and the lamp has a luminous intensity of 3.0 mcd at 10 mA dc:

Figure 2.4.2-1: $I_V = (2.48)(3.0)(.10) = .74 \text{ mcd}$

Figure 2.4.2-2: $I_V = \frac{(20)(.10)(1.24)(3.0)}{(10)(1.00)(1.00)} = .74 \text{ mcd}$

Figure 2.4.2-3: $I_V = \frac{(1.0)(3.0)}{4.0} = .75 \text{ mcd}$

2.4.3 Driving an LED Lamp

2.4.3.1 LED Electrical Characteristics

Figure 2.4.3.1-1 shows typical electrical characteristics for standard red, high efficiency red, yellow and green lamps. Above 1.5 volts V_F , the current flowing through an LED increases very rapidly. The dynamic resistance can be considered to be the slope of the diode characteristic ($\Delta V_F / \Delta I_F$) in the forward region. The standard red lamp has a very low dynamic resistance, while the high efficiency red, yellow and green lamps have a somewhat higher dynamic resistance. Since the dynamic resistance is so small, LED lamps should not be connected in parallel. Small variations in V_F or dynamic resistance can cause current hogging by the LED with the lowest V_F . This current hogging can cause variations in luminous intensity and excessive power dissipation in the lamp. However, LED

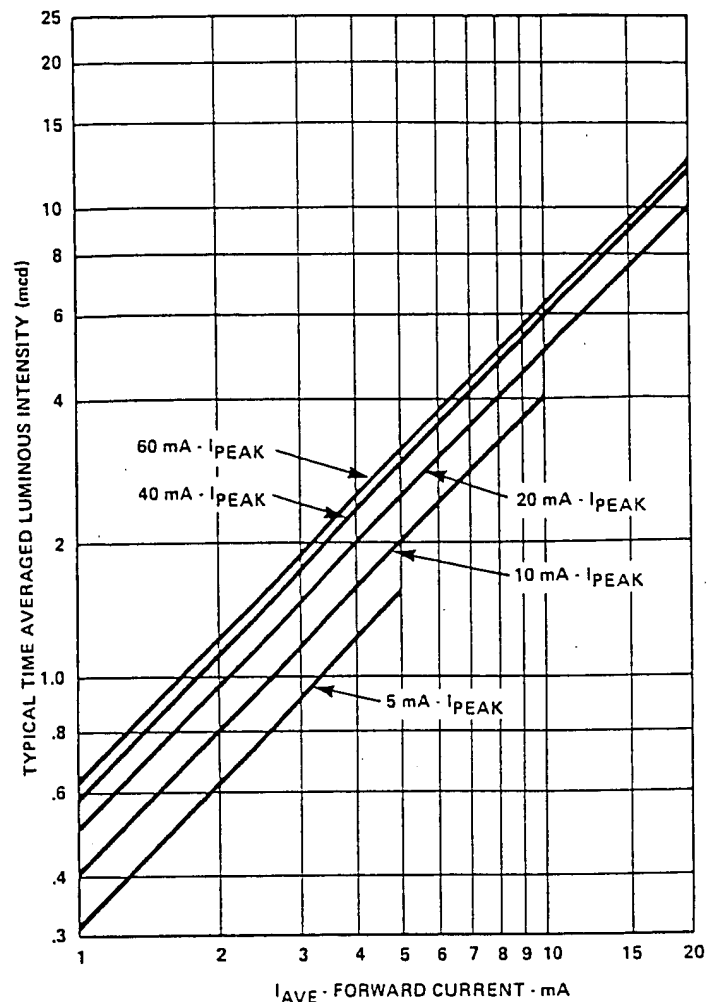
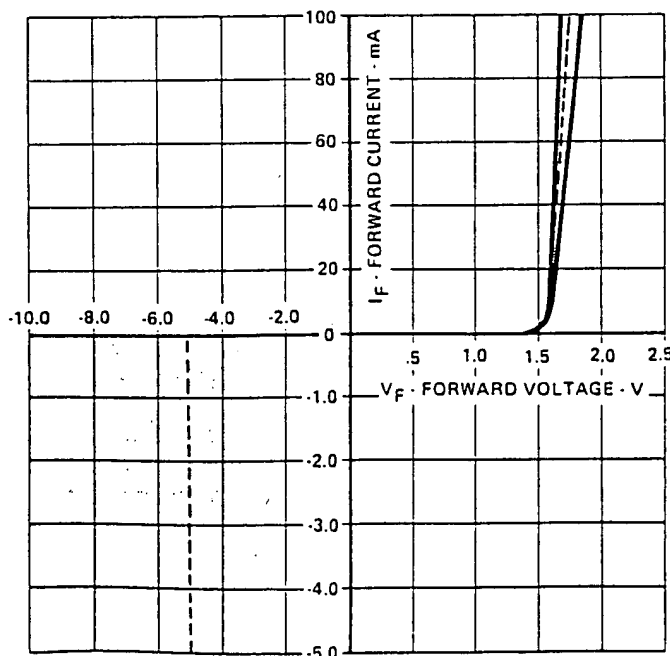
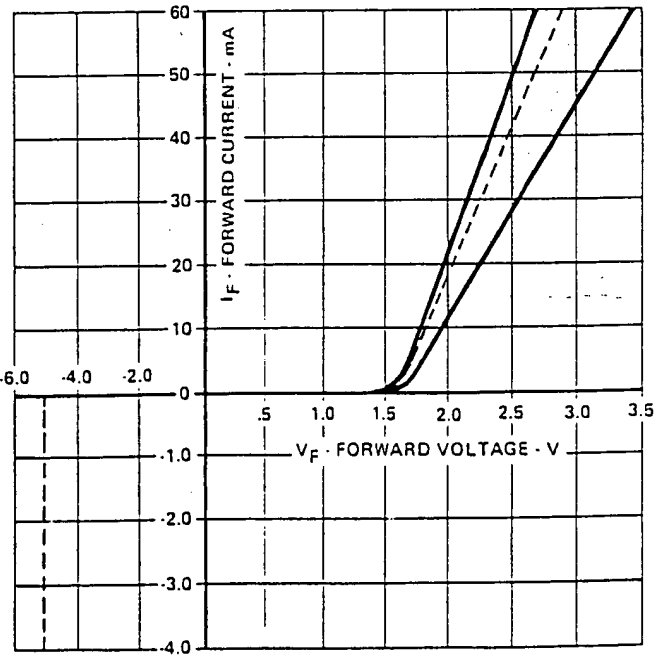


Figure 2.4.2-3 Typical Time Averaged Luminous Intensity vs. Average Current for a High Efficiency Red LED.



STANDARD RED LAMP (GaAsP SUBSTRATE)



HIGH EFFICIENCY RED, YELLOW, GREEN LAMP (GaP SUBSTRATE)

Figure 2.4.3.1-1 Typical Electrical Characteristics of LED Lamps.

lamps can be connected in series as long as the combined V_F doesn't exceed the power supply potential.

Negligible current flows through an LED in the reverse direction until the breakdown voltage is exceeded. Above the breakdown voltage, BV_R , the reverse current increases very rapidly, such as shown in Figure 2.4.3.1-1. Exceeding the BV_R will not harm the LED lamp as long as the reverse current is externally limited to prevent excessive power dissipation in the LED. When several LED lamps are connected in an array, reverse leakage current can cause unwanted ghosting of normally off LED's. This can be prevented by using LED drivers with a high impedance off state.

2.4.3.2 Resistive Current Limiting

When LED lamps are driven from a regulated power supply, a resistor can be used to limit the current flowing through the LED. The LED current, I_F , is determined by the following equation:

$$I_F = \frac{V_{CC} - V_F - V_{CE\ SAT}}{R} \quad (2.4.3.2-1)$$

where V_{CC} is the power supply potential, $V_{CE\ SAT}$ is the "on" voltage of the LED driver, I_F and V_F are the forward characteristics of the LED, and R is the current limiting resistor.

NUMERICAL SOLUTION:

$$I_{F(max)} = \frac{V_{CC(max)} - V_{CE\ SAT(min)} - V_{F(min)}}{R(min)}$$

$$I_{F(min)} = \frac{V_{CC(min)} - V_{CE\ SAT(max)} - V_{F(max)}}{R(max)}$$

EXAMPLE:

ASSUME $V_{F(min)} = 1.62 @ 20\text{ mA}$

$V_{F(max)} = 1.67 @ 20\text{ mA}$

$V_{CC} = 5.0V \pm 10\%$

$R = 180\Omega \pm 10\%$

$V_{CE\ SAT} = .2V @ 20\text{ mA}$

$$1. \quad V_{CC} = 4.5V, I_{F(max)} = \frac{4.50 - .20 - 1.62}{(180)(.9)} = 16.5\text{ mA}$$

$$I_{F(min)} = \frac{4.50 - .20 - 1.67}{(180)(1.1)} = 13.3\text{ mA}$$

$$2. \quad V_{CC} = 5.0V : 15.8\text{ mA} < I_F < 19.6\text{ mA}$$

$$3. \quad V_{CC} = 5.5V : 18.3\text{ mA} < I_F < 22.7\text{ mA}$$

If V_{CC} is considerably larger than $V_F + V_{CE\ SAT}$, then small variations of V_F or $V_{CE\ SAT}$ will have only negligible effects on I_F . However, variations of V_{CC} or R will cause a corresponding variation of I_F . For example, suppose that a standard red LED lamp is to be driven from a 5.0V supply by a transistor switch with a $V_{CE\ SAT}$ of .2V at 18 mA I_F . Then the nominal value of resistance is equal to:

$$R = \frac{5.0 - .2 - 1.65}{18} = 175\Omega$$

Figure 2.4.3.2-1 shows both numerical and graphical solutions to this example assuming tolerances of V_{CC} , R , $V_{CE\ SAT}$, and V_F .

Resistor-LED lamps are also available that are designed to operate off of a 5 volt supply.

2.4.3.3 Constant Current Limiting

In some applications, it may be desirable to drive LED lamps with a current source. The current source can be used to regulate the current through the LED regardless of power supply variations or variations in V_F between LED lamps. Figure 2.4.3.3-1 shows some examples of simple current sources constructed of npn transistors. For both circuits, the current through the LED string remains constant as long as V_{CC} is greater than $V_{CC(min)}$. The

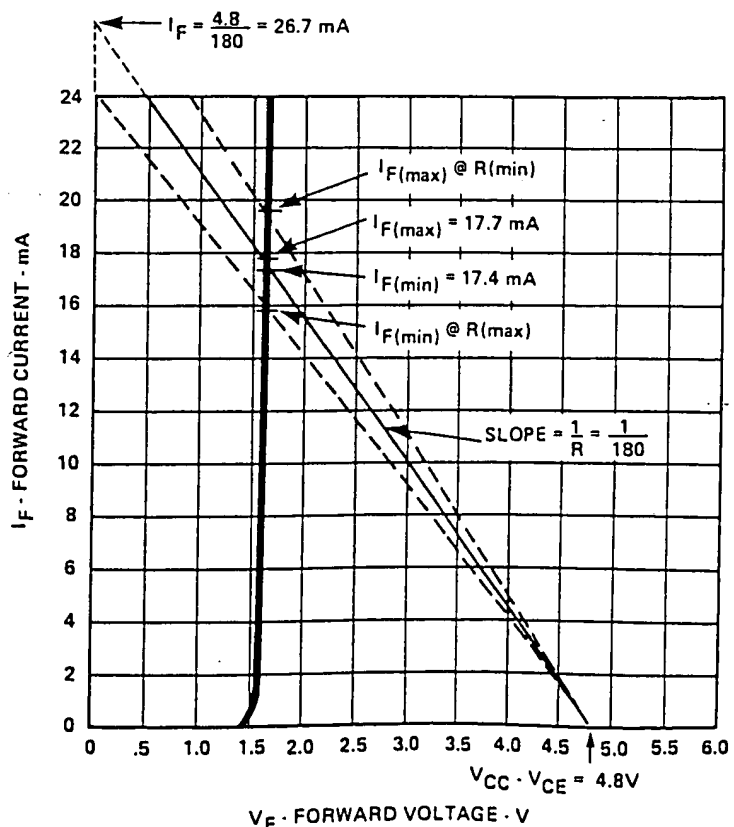


Figure 2.4.3.2-1 Numerical and Graphical Solution to Resistive Current Limiting.

first circuit uses an LED lamp as a voltage reference. Since $\Delta V_F(T) \cong \Delta V_{BE}(T) \cong -2 \text{ mV}/^\circ\text{C}$, the current source will remain stable over temperature. In the second circuit, since V_{BE} varies with temperature, the current source will also vary with temperature. However, this change is typically about:

$$\frac{\Delta V_{BE}/\Delta T}{V_{BE}} \cong \frac{-2}{650} \cong -0.3\%/^\circ\text{C}$$

Commercially available current regulator IC's can also used.

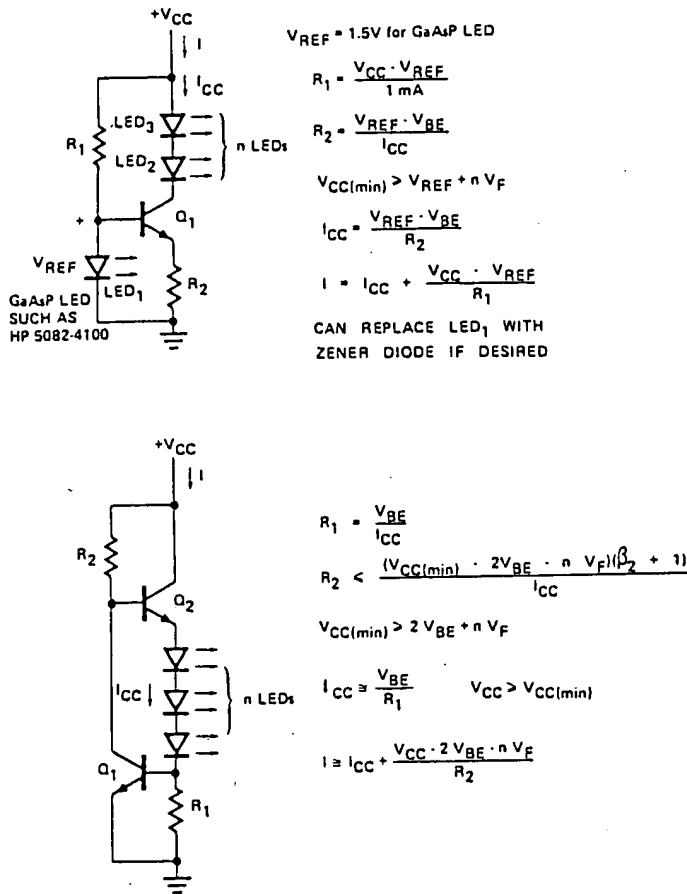


Figure 2.4.3.3-1 Some Examples of Constant Current LED Drivers that Regulate I_{CC} Regardless of V_{CC} .

2.4.3.4 LED-Logic Interface

Since LED lamps operate at low voltages and currents, they can interface to most digital logic families directly. Figure 2.4.3.4-1 shows some of the ways that an LED lamp can be used to interface to digital logic. TTL logic families are guaranteed to sink a minimum amount of current (I_{OL}) which can drive most LED lamps without additional buffering. Table 2.4.3.4-1 lists the guaranteed I_{OL} of most common TTL families:

74S: $I_{OL} \leq 20 \text{ mA}$	74LS: $I_{OL} \leq 8 \text{ mA}$
74H: $I_{OL} \leq 20 \text{ mA}$	74L: $I_{OL} \leq 3.6 \text{ mA}$
74: $I_{OL} \leq 16 \text{ mA}$	

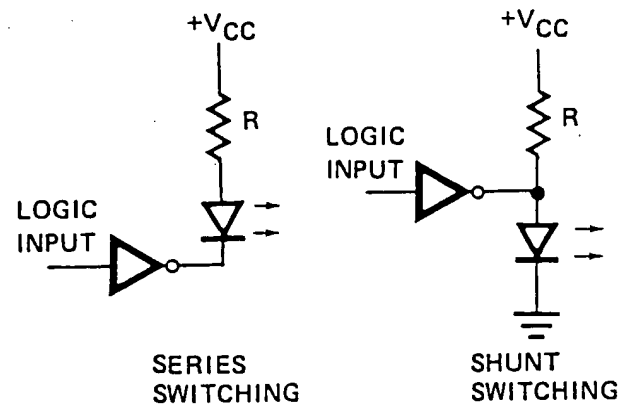
TABLE 2.4.3.4-1 TTL Interface

If additional current is required, such as in a strobed application, TTL buffers are also available. CMOS buffers can also drive many LED lamps directly. Table 2.4.3.4-2 lists some of the commonly available CMOS buffers:

RCA 4049, 4050	$I_{OL} \geq 3 \text{ mA}, V_{OL} = .4 \text{ V}, V_{CC} = 5 \text{ V}$
4009, 4010	$I_{OL} \geq 8 \text{ mA}, V_{OL} = .5 \text{ V}, V_{CC} = 10 \text{ V}$
National 74C906	$I_{OL} \sim 8 \text{ mA}, V_{OL} = .5 \text{ V}, V_{CC} = 4.75 \text{ V}$
	$I_{OL} \sim 20 \text{ mA}, V_{OL} = .5 \text{ V}, V_{CC} = 10 \text{ V}$
National 74C901	$I_{OL} \geq 3.8 \text{ mA}, V_{OL} = .4 \text{ V},$
74C902	

TABLE 2.4.3.4-2 CMOS Interface

OPEN COLLECTOR GATES



ACTIVE PULLUP - TOTEM POLE GATES

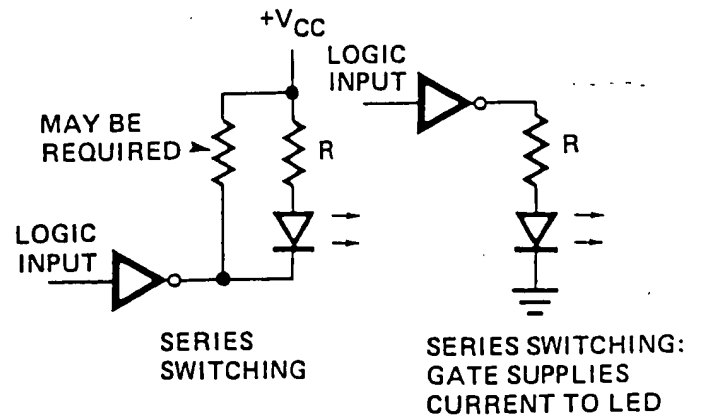


Figure 2.4.3.4-1 Digital Logic Can Interface Directly to LED Lamps.



US005936599A

United States Patent [19]

[11] Patent Number: 5,936,599

Reymond

[45] Date of Patent: Aug. 10, 1999.

[54] AC POWERED LIGHT EMITTING DIODE
ARRAY CIRCUITS FOR USE IN TRAFFIC
SIGNAL DISPLAYS

[76] Inventor: Welles Reymond, 380 Hitchcock Rd,
#84, Waterbury, Conn. 06705

[21] Appl. No.: 09/078,072

[22] Filed: May 13, 1998

Related U.S. Application Data

[63] Continuation of application No. 08/379,973, Jan. 27, 1995,
abandoned.

[51] Int. Cl.⁵ G09G 3/36

[52] U.S. Cl. 345/82; 345/46

[58] Field of Search 345/82, 83, 46,
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Primary Examiner—Richard A. Hjerpe

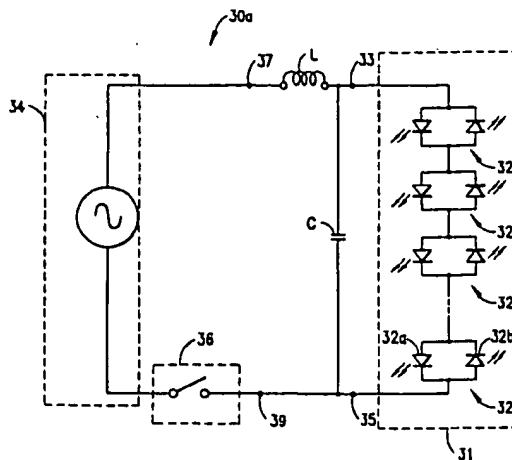
Assistant Examiner—Ricardo Osorio

Attorney, Agent, or Firm—David P. Gordon; David S.
Jacobson; Thomas A. Gallagher

ABSTRACT

[57] An LED array circuit includes a number of series connected LED pairs, each pair including two parallel connected oppositely polarized LEDs. The array is coupled to a standard AC voltage source in series with an inductor L having $Q > 5$ and a reactance which is equivalent to the resistance of a current limiting resistor. The use of an inductor in place of a resistor increases the efficiency of the array to approximately 80%. The efficiency of the array is increased even further by coupling a capacitor parallel to the array and by tuning the inductor and capacitor to the frequency of the AC voltage source. According to one embodiment of the invention, a somg,c retro-fittable unit is provided wherein an inductor, a capacitor, and an array of LEDs are contained in a housing having substantially the same size and shape as a standard incandescent bulb or the lens/filter used in a traffic signal display. According to another embodiment of the invention, a single module is provided with a plurality of LED arrays, with each LED array having its own capacitor coupled in parallel thereto, and its own series coupled switch. The module is coupled to and across the AC voltage source, with one node of the module coupled to the AC voltage source by an inductor.

29 Claims, 5 Drawing Sheets



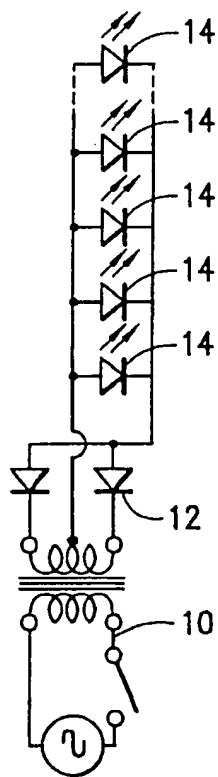


FIG. 1
PRIOR ART

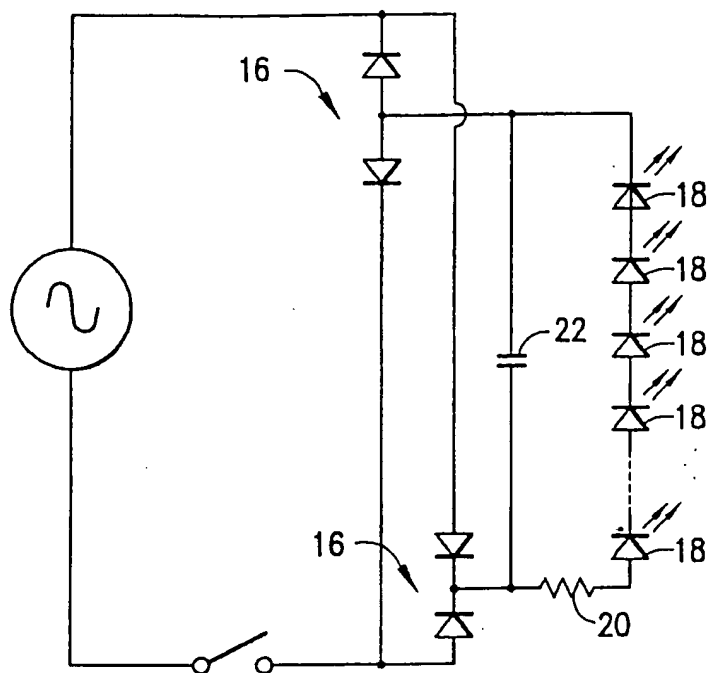


FIG. 2
PRIOR ART

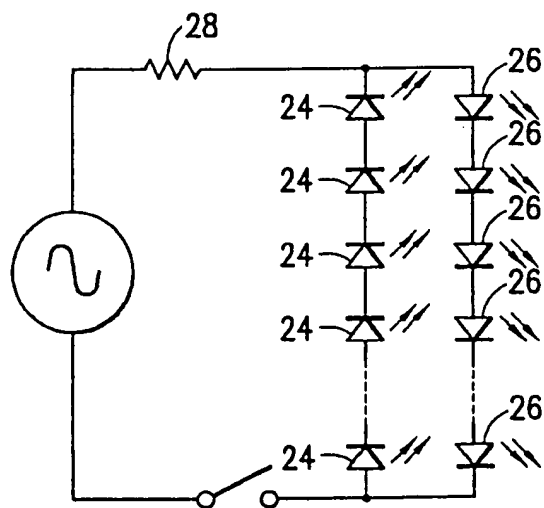


FIG. 3
PRIOR ART

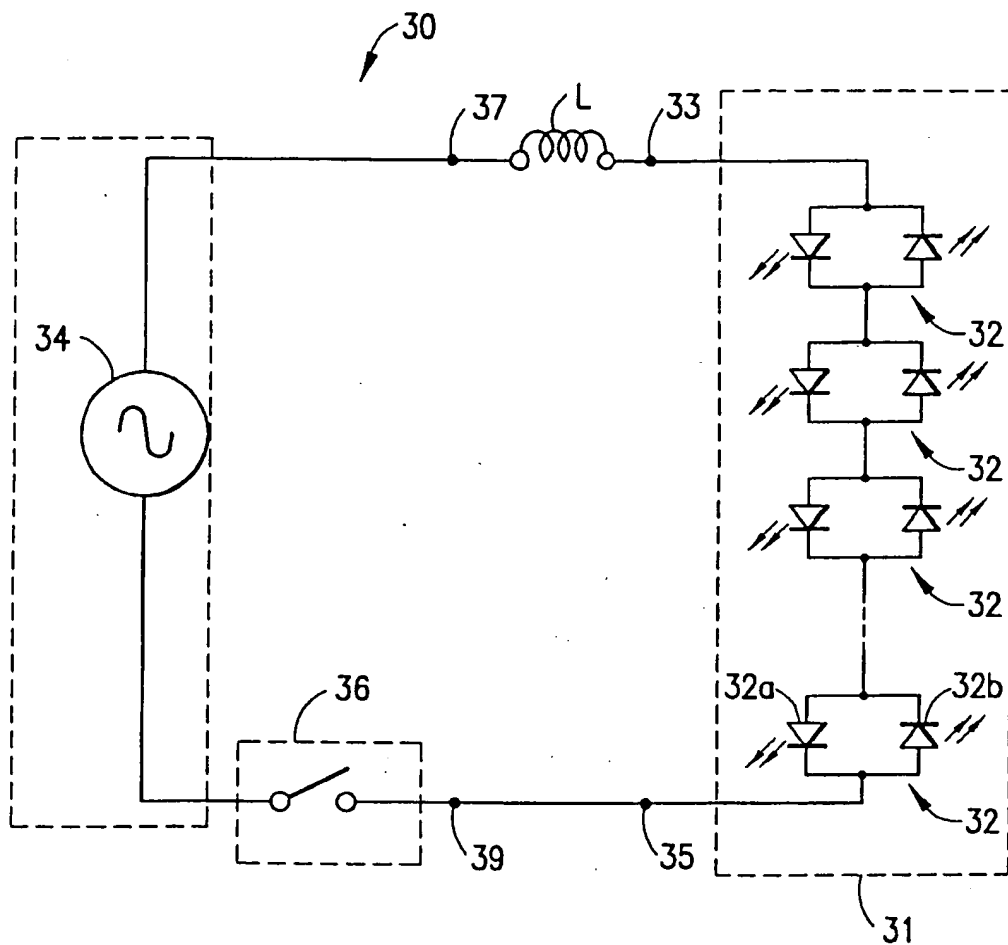


FIG.4

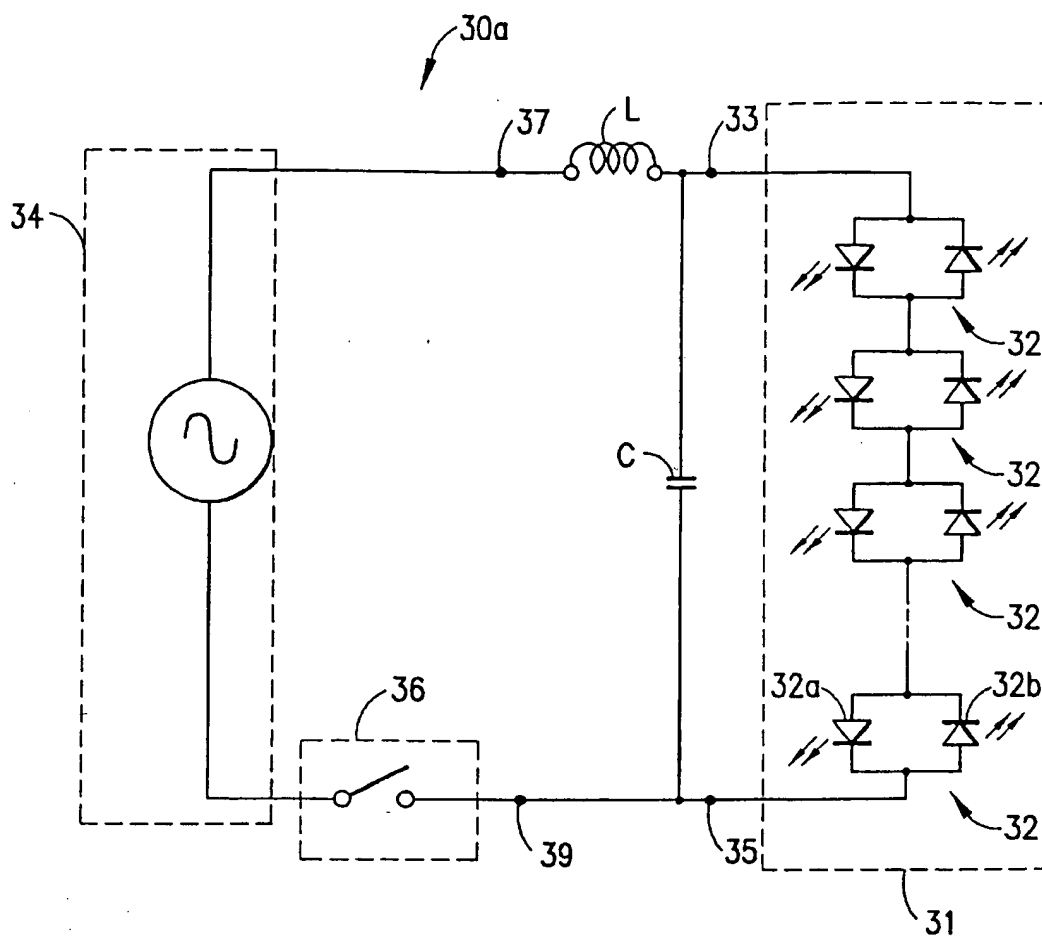


FIG. 5

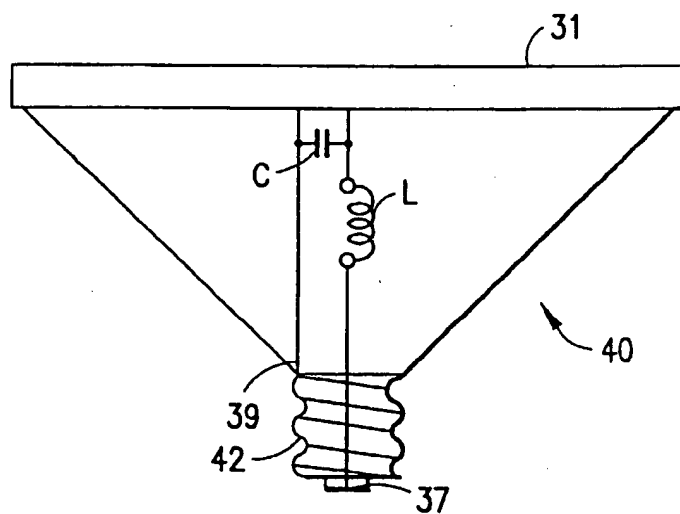


FIG. 6

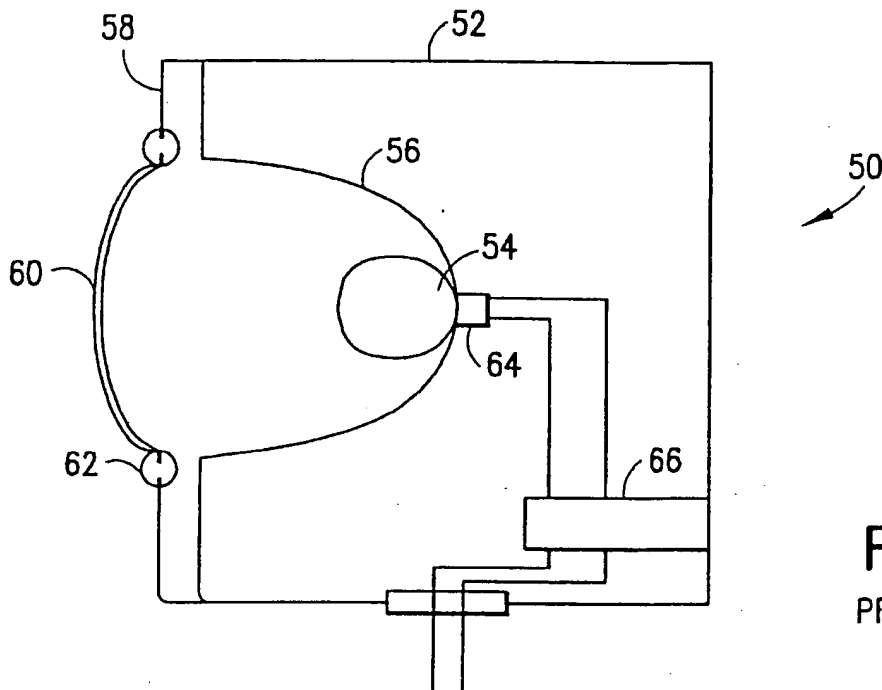


FIG. 7
PRIOR ART

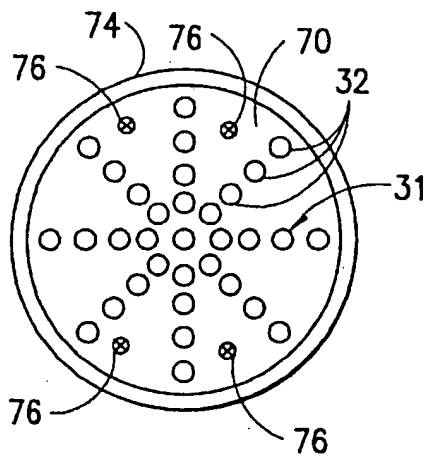


FIG. 8

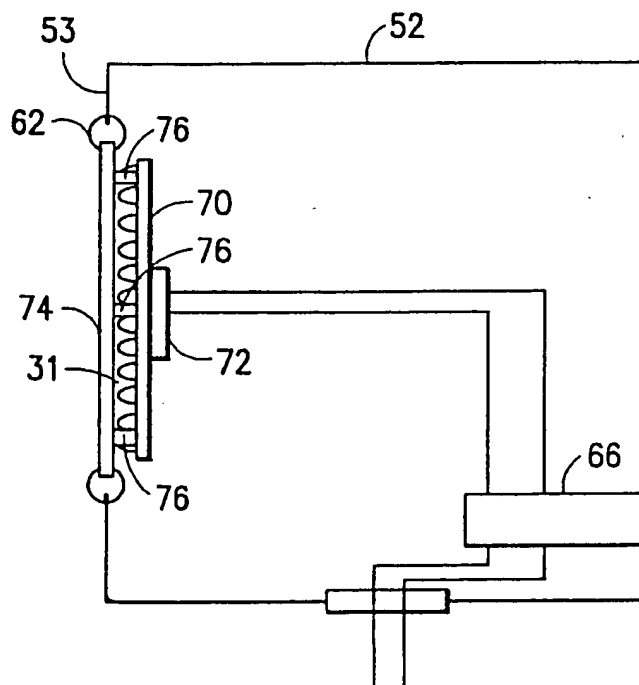


FIG. 9

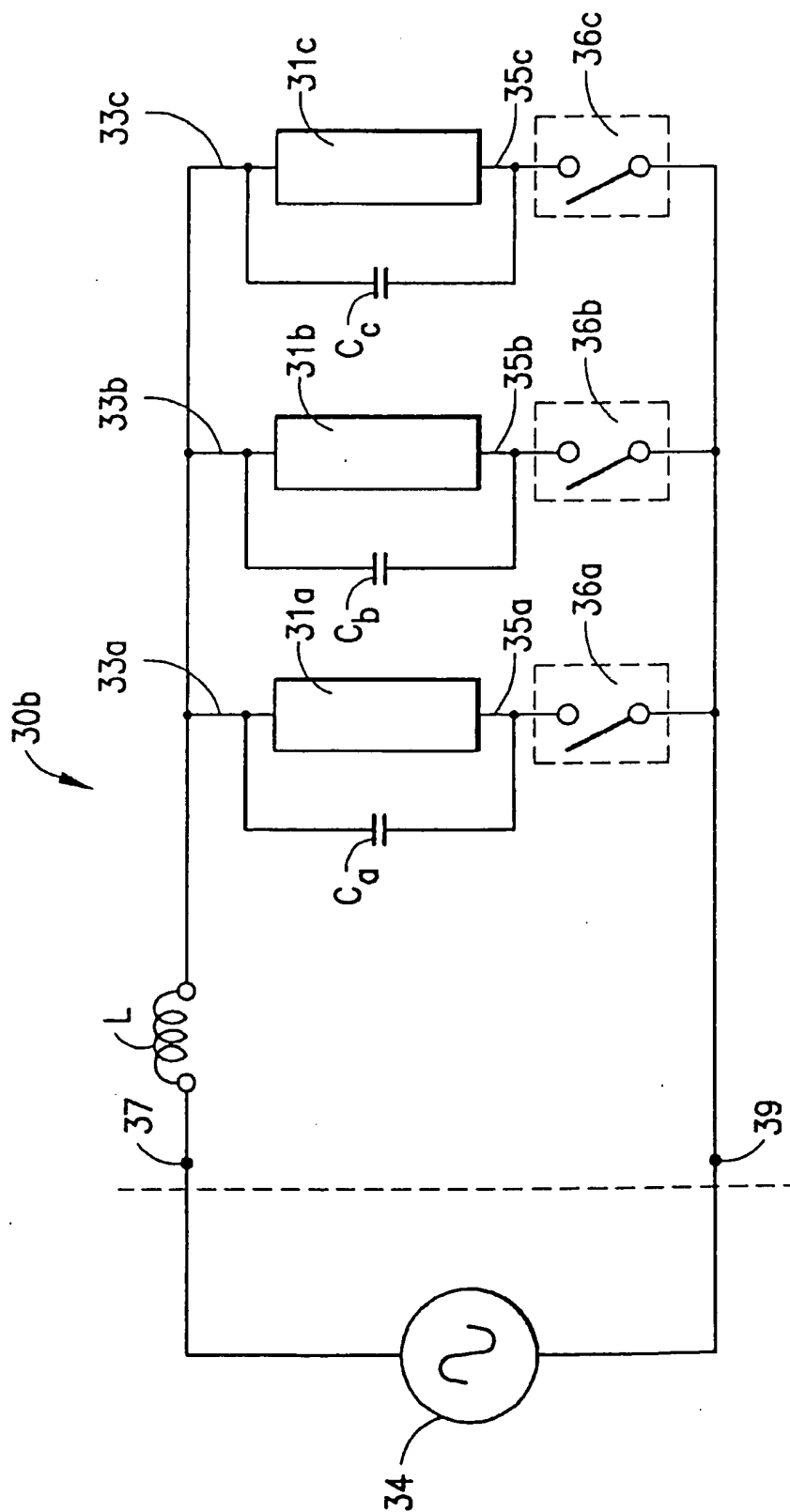


FIG. 10

AC POWERED LIGHT EMITTING DIODE ARRAY CIRCUITS FOR USE IN TRAFFIC SIGNAL DISPLAYS

This is a continuation of presently U.S. Ser. No. 08/379, 5
973, filed Jan. 27, 1995, now abandoned.

BACKGROUND OF THE INVENTION

1. Field of the Invention

The invention relates to light emitting diode arrays. More particularly, the invention relates to circuits incorporating light emitting diode arrays which are powered by an alternating current and which are advantageously used in traffic signal and other displays.

2. State of the Art

Modern traffic signal systems include two major components: the controller and the display (lights). The technology of modern traffic signal controllers is quite evolved and includes modern computer technology which incorporates traffic flow sensors, timers, and the like. Over the last seventy years, however, traffic signal displays have not changed significantly. The displays utilize high power broad spectrum incandescent bulbs with colored filters to produce the desired traffic signal color. It is well known that traffic signal displays are inefficient, as they consume large amounts of energy in order to produce a display which is bright enough to be seen in broad daylight. The inefficiency of the displays is due in part to the general inefficiency of incandescent bulbs, and is exacerbated by the fact that much of the light energy produced by the bulbs is wasted by filtering the light. Moreover, traffic signal displays require frequent maintenance. Incandescent bulbs have a relatively short life span, typically less than eight thousand hours, shorter still if switched on and off frequently and if constantly exposed to the elements; all of which are the case with traffic signals.

It is known in the art to use a light emitting diode (LED) array in lieu of incandescent bulbs in a traffic signal. Such arrays are disclosed, for example, in U.S. Pat. No. 4,271,408 to Teshima et al., U.S. Pat. No. 4,298,869 to Okuno, and U.S. Pat. No. 4,954,822 to Borenstein, the complete disclosures of which are hereby incorporated herein by reference. An LED array can provide many advantages when used in lieu of an incandescent bulb. The primary advantages are that an LED array is much more efficient than an incandescent bulb and requires little or no maintenance. In most cases, an LED array will consume about one tenth the power that a filtered incandescent bulb will consume to produce the same light output. The life cycle costs of a traffic signal using an LED array in lieu of an incandescent bulb is also significantly reduced since incandescent bulbs used in traffic signals typically must be replaced once or twice a year. A well designed LED array could be expected to function for more than twenty years before requiring replacement. Another, less apparent advantage is that a single array can be used to display many different illuminated symbols such as international symbols for turn only, do not enter, walk, don't walk, etc. The LED array is more resistant to the elements and is more mechanically durable than an incandescent bulb. It is also possible to achieve a higher flashing rate with an LED array than with an incandescent bulb. It is known in industrial psychology that certain high flashing rates are more apt to draw attention than other slower flashing rates. In addition, an LED array does not require a light reflector like the relatively large parabolical reflectors used with incandescent bulbs. The elimination of the reflector is an

advantage because during certain seasons at certain times of day, sunlight can be reflected off the reflector in an incandescent bulb traffic signal and cause a confusing display. Yet another advantage of an LED array is that, if it is properly arranged, when faults develop in the array, the entire array need not fail.

Despite all of the advantages of using LED arrays in traffic signal displays, there are several concerns which have prevented their widespread adoption. The first and perhaps the most significant concern is that an LED array is not easily retro-fitted to an existing traffic signal. This is primarily because existing incandescent displays operate with a "standard" 120 volt 60 Hz AC power supply. LEDs require a DC current of approximately 5 to 20 milliamps and a forward operating voltage of between 1.5 to 2.5 volts depending on the wavelength of the emitted light and the semiconductor material used. Another reason why retrofitting is difficult is because the "standard" traffic signal housings are designed to accept a "standard" incandescent bulb. These issues have been addressed in the art. As shown in prior art FIG. 1, an arrangement which has been proposed by Borenstein, supra., uses a step down isolation transformer 10 with a center tapped full-wave rectifier 12 to drive an array of LEDs 14 which are connected in parallel. Although Borenstein does not specify exactly how many LEDs are to be used, a typical traffic signal display will require between twenty and eighty LEDs. Assuming that fifty LEDs are used with Borenstein's power supply, it is difficult to imagine that an efficiency of more than 50% could be achieved. Moreover, the most common LED failure mode is a short where the LED becomes a short circuit. If the LEDs are arranged in parallel as taught by Borenstein, a short fault in one LED will disable the entire array.

As shown in prior art FIG. 2, a simpler arrangement which has been proposed by Teshima et al., supra., uses a rectifier bridge 16 to convert the AC power supply to pulsating DC and an array of sixty-two 1.6 volt LEDs 18 in series with a resistor 20. A smoothing capacitor 22 is connected in parallel with the array for absorbing ripple components of the power supply. Unfortunately, the rectifier circuit adds expense to the system and makes it less reliable. The resistor wastes energy and lowers the efficiency of the system. While Teshima et al. suggests that the rectifier can be eliminated by using pairs of oppositely polarized LEDs connected in series through a protective resistor, little information is given about this arrangement.

A simpler solution has been proposed by Okuno, supra., which is shown in prior art FIG. 3. Okuno avoids the use of a rectifier bridge by providing an array of LEDs 24 which are connected in series and polarized in one direction and an array of LEDs 26 which are connected in series and polarized in the opposite direction. The two arrays 24 and 26 are connected in parallel so that a respective array is illuminated during each half cycle of the AC power supply. According to Okuno, however, a current limiting resistor 28 (a generator resistor) must be connected in series with the arrays. Assuming each array 24 and 26 includes twenty-five LEDs, the value of the resistor 28 should be approximately 3300 ohms to produce the desired average LED current. Since approximately 70% of the line voltage is dropped across the resistor 28, the resistor is the dominant factor in determining the LED current and energy is wasted by the resistor. In this example, the arrangement has an efficiency of only about 35% and the LED current has a range of $\pm 25\%$. If a greater number of LEDs were used, the efficiency would increase, but the current range would widen.

SUMMARY OF THE INVENTION

It is therefore an object of the invention to provide a circuit incorporating an AC line powered LED array which is suitable for retrofitting in an existing traffic signal display.

It is also an object of the invention to provide an AC line powered LED array circuit which does not require a rectifier bridge or a transformer.

It is another object of the invention to provide an AC line powered LED array circuit which has enhanced efficiency and does not use a current limiting resistance.

It is a further object of the invention to provide a highly efficient, low cost AC powered LED array circuit for use in traffic signal and other displays.

It is also an object of the invention to provide an AC line powered LED array having a large number of LEDs connected in series so that the cumulative voltage across the array may be comparable to or greater than an AC line voltage.

It is another object of the invention to provide an AC line powered LED array circuit which provides a relatively constant current through the LED array regardless of the type or number of LEDs in the array.

It is a further object of the invention to provide an AC line powered LED array and circuit which is highly fault tolerant.

It is still another object of the invention to provide AC line powered LED display which is compatible with existing traffic signal controllers.

In accord with these objects which will be discussed in detail below, the LED array circuit of the present invention includes a number of series connected LED pairs, each pair including two parallel connected oppositely polarized LEDs, which are coupled to a standard AC voltage source by an inductor which is arranged in series between the AC voltage source and the LED array. The inductor is preferably provided with a $Q > 5$ and a reactance which is equivalent to the resistance of a current generator or current limiting resistor. The use of an inductor in place of a resistor increases the efficiency of the array circuit to approximately 80% if the inductor is properly chosen. The efficiency of the array circuit is increased even further by coupling a capacitor in parallel to the array, thereby generating an impedance converter which converts to AC voltage source into a high impedance AC current source. By tuning the inductor and capacitor of the impedance converter to the frequency of the AC voltage source, the efficiency of the array is greater than 80%. Moreover, when the capacitor is included in the circuit, the power factor of the circuit is improved, non-linearity of the circuit is diluted, the impedance of the source is increased, and the LED array may include a large number of LEDs (e.g., forty pairs or more). In fact, so many LEDs may be included in the array such that the voltage drop across the array is greater than the AC line peak voltage itself.

According to a preferred embodiment of the invention, the LED array and circuit are mounted on a circuit board which is connected by spacers to a clear circular disk. The disk is dimensioned to take the place of a standard traffic signal filter/lens. This embodiment is retro-fitted to an existing traffic signal by removing the bulb, reflector, and filter/lens from the traffic signal and mounting the clear circular disk in place of the filter. Alternatively, and in accord with another embodiment of the invention, a single retro-fittable unit is provided wherein an inductor, a capacitor, and an array of LEDs are contained in a housing having substantially the same size and shape as a standard incandescent bulb used in a traffic signal display. According to yet another embodiment of the invention, a plurality of individually switched arrays are contained in a single module where a first terminal of each array is coupled to a common point which is series connected through a single

inductor to the AC voltage source, and a separate capacitor is coupled parallel to each array. The second terminal of each array is coupled through a respective individual switch to the AC voltage source. The second embodiment provides a module for several independently operable mutually exclusive displays.

Additional objects and advantages of the invention will become apparent to those skilled in the art upon reference to the detailed description taken in conjunction with the provided figures.

BRIEF DESCRIPTION OF THE DRAWINGS

FIG. 1 is a schematic diagram of a prior art LED array circuit using a transformed and rectified power supply with LEDs coupled in parallel;

FIG. 2 is a schematic diagram of a prior art LED array circuit using a filtered and rectified power supply with LEDs coupled in series with a current limiting resistor;

FIG. 3 is a schematic diagram of a prior art LED array circuit with oppositely polarized series connected LEDs coupled in series with a current limiting resistor;

FIG. 4 is a schematic diagram of a first embodiment of an LED array circuit according to the invention;

FIG. 5 is a schematic diagram of a second embodiment of an LED array circuit according to the invention;

FIG. 6 is a partially transparent side elevation view of a housing for the LED array circuit of FIG. 5 which is adapted for retro-fitting in an existing traffic signal display;

FIG. 7 is a cross sectional schematic view of a prior art traffic signal display having an incandescent bulb, a reflector, and a colored filter/lens;

FIG. 8 is a side elevation schematic view of an LED array according to the invention mounted on a circular disk for retro-fitting in an existing traffic signal display;

FIG. 9 is a cross sectional view similar to FIG. 7 showing the LED array of FIG. 8 installed in an existing traffic signal display; and

FIG. 10 is a schematic diagram of a third embodiment of an LED array circuit according to the invention.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

Referring now to FIG. 4, a first embodiment of an LED array circuit 30 according to the invention includes an array of LEDs 31 arranged as a plurality of LED pairs 32, and an inductor L. The LED pairs 32 each include two parallel coupled LEDs 32a, 32b which are oppositely polarized. The LED pairs 32 are coupled to each other in series to form the LED array 31. A first terminal or node 33 of the array is coupled to an AC voltage source 34 through the series connected inductor L, with the second terminal or node 35 of the array 31 coupled to the AC voltage source through a switch 36. It will be appreciated that the switch 36 shown in FIG. 4 is merely representative of some type of switching circuit and in practice will likely be part of a traffic signal controller. It will also be understood that the connection of the circuit 30 to the AC voltage source 34 is preferably a removable connection as represented in FIG. 4 by removable couplings 37 and 39. According to the invention, the inductor L is chosen to have a reactance equivalent to the resistance of a current generator or current limiting resistor and to have $Q > 5$. The use of inductor L with the array 30 produces an efficiency of about 80% and achieves all of the advantages of LED arrays without suffering the disadvantages of the current limiting resistor of the prior art.

It will be appreciated that during one half cycle of the AC voltage source, one of the LEDs in each pair will light and during the other half cycle, the other LED in each pair will light. One of the advantages of arranging the LEDs as shown (i.e. in parallel oppositely polarized pairs which are series connected) is that if an LED faults either closed (short) or open, only that LED or one LED pair will be disabled. That is, if an LED shorts, all of the remaining LEDs will continue to function except for the one which is paired with the shorted LED and which will be shorted thereby. If, on the other hand, a red LED faults open, the LED which is paired with it will be forced to conduct reverse voltage during the half cycle in which the open faulted LED would have lit.

Those skilled in the art should appreciate that the circuit of FIG. 4 exhibits non-linear characteristics and that the most important factor in powering the LEDs is the average current which flows through the array of LEDs. If the voltage drop across the LED array is small relative to the peak line voltage, the current through the array is substantially related to the RMS short circuit current I_{SC} through the inductor L which is expressed below according to the approximation:

$$I_{SC} \approx \frac{V}{Z_L} \quad (1)$$

where V is the RMS line voltage and Z_L is the impedance of the inductor L. Since it is the average current rather than the RMS current which is of importance to the LEDs, the average short circuit current $I_{SC(AVG)}$ through the inductor L over one complete AC cycle is expressed according to approximation:

$$I_{SC(AVG)} \approx \left(\frac{2\sqrt{2}}{\pi} \right) I_{SC} \quad (2)$$

It will also be appreciated that the impedance Z_L of the inductor L is a complex number related to its inductance L by:

$$Z_L = j2\pi FL \quad (3)$$

where F is the AC line frequency and j is the square root of (-1). By combining the above approximations (1) and (2) and equation (3), the average short circuit current through the inductor L can be expressed according to:

$$I_{SC(AVG)} \approx \left(\frac{2\sqrt{2}}{\pi} \right) \left(\frac{V}{j2\pi FL} \right) \approx \frac{V\sqrt{2}}{\pi^2 FL} \quad (4)$$

Since each LED is ON for a half cycle and OFF for a half cycle, the average current through current $I_{LED(AVG)}$ through the array during each half cycle will be substantially equal to one half the average short circuit current $I_{SC(AVG)}$ through the inductor L so long as the voltage drop across the LED array is relatively small compared to the peak AC line voltage. For example, utilizing the relationship (4) above with an AC source of 120 V RMS at 60 Hz, an appropriate average current of approximately 24 ma through an array of approximately twenty LEDs every half cycle can be controlled by an inductor L having an inductance of 6 Henries. The inductor L will maintain an appropriate average current through the array so long as the voltage drop across the array is relatively small as compared to the peak AC line voltage.

The efficiency, power factor, and control of current in the circuit of FIG. 4 can be further enhanced by the addition of

a capacitor C as shown in FIG. 5. The circuit 30a of FIG. 5 is substantially the same as that in FIG. 4, but with the addition of capacitor C which is coupled in parallel to the LED array 31 across the terminals 33 and 35. In this circuit, the inductance of the inductor L is preferably chosen according to the relationship (4) given above. The L-C circuit shown in FIG. 5 is not a filter circuit but is an impedance converter which effectively converts the AC voltage source into a high impedance AC current source when the L-C circuit is tuned to the frequency of the AC source according to approximation:

$$F \approx \frac{1}{2\pi\sqrt{LC}} \quad (5)$$

Thus, the value of the capacitor C is preferably chosen according to the approximation:

$$C \approx \frac{1}{(2\pi F)^2 L} \quad (6)$$

with a frequency of 60 Hz, and an inductor of approximately 6 Henries, the desired capacitance would be approximately 1 μ F. This arrangement effectively increases the current generator impedance Z_g of the circuit by a factor of Q such that $|Z_g| = QX_L$, with X_L being the reactance of the inductor L. It also increases the open circuit voltage V_{OC} by a factor of Q so that $V_{OC} = QV$. The AC voltage source therefore appears to the LED array as a current source even when the voltage drop across the array is comparable to the peak AC line voltage. Because of the high current generator impedance, the same tuned circuit can tolerate a wide range in the number and types of LED pairs without materially affecting the LED current. Thus, a standard tuned circuit can be used with many different types of LED arrays. Moreover, in principle, the tuned circuit can generate a voltage across the LED array which may be greater than the AC line voltage. Therefore, a very large number of LEDs can be used in the array. Indeed, in a preferred embodiment of the invention, forty or more pairs of LEDs are utilized. It should be noted that the inductor L, when used in the AC powered circuits described above, provides high impedance without energy wasting resistance.

Referring now to FIG. 6, those skilled in the art will appreciate that the LED array 31, the inductor L and the capacitor C can be mounted in a housing 40 having the same size and shape as a conventional incandescent bulb with a conventional base connector 42. In this manner, the array is easily retro-fitted to existing traffic signal displays which utilize this type of fixture.

The invention may be easily adapted to replace incandescent lighting in virtually any kind of traffic signal display unit. FIG. 7 shows a popular existing traffic signal display 50 having a weather tight enclosure or casing 52 which contains an incandescent bulb 54 and a parabolic reflector 56. The inside of the enclosure 52 is accessible via a hinged door 58 which carries a colored lens/filter 60 (e.g. red, yellow, or green) fitted to an opening in the door with a grommet 62. The bulb 54 is held in a socket 64 which is electrically coupled to a voltage source (not shown) via a quick connect block 66. The bulb 54, reflector 56, socket 64 and attached wires are also hinged to the enclosure 52. Thus the interior elements of the display 50 are all easily accessible and replaceable.

According to a preferred embodiment of the invention, and as shown in FIGS. 8 and 9, an LED array 31 is mounted

on one side of a circuit board 70 which is provided with circuit traces and elements 72 on its other side. The circuit elements and traces may include the inductor L, the capacitor C, and the connections of the array 31 as described with reference to FIGS. 4 and 5 above. The circuit board 70 is coupled to a clear plastic disk 74 by a number of spacers 76 so that the LEDs 32 in the array 31 face the disk 74 as seen best in FIG. 9. Preferably, both the disk 74 and the circuit board 70 are circular. The disk 72 is fitted with a grommet 62 which is substantially the same as the grommet 62 used to hold the filter/lens 60 in the prior art display 50 described above. The prior art display 50 of FIG. 7 is modified by removing the lens/filter 60 with its grommet 62 and by removing the bulb 54, reflector 56, and socket 64. The disk 74 with its grommet 62 is fitted into the opening in the hinged door 53 and the circuit 72 is electrically coupled to the quick connect block 66. It has been found that the portion of the circuit board 70 which faces the disk 74 should be painted black before mounting the LEDs 32. This prevents unwanted reflection off the circuit board during bright daylight hours. It will be appreciated that the circuit 72 need not be mounted on the circuit board 70. All or part of the circuit 72 could be mounted off the board 70 inside the enclosure 52. As a practical matter, it may be advantageous to mount all of the circuit except for the inductor L on the circuit board 70 and mount the inductor L inside the enclosure 52.

FIG. 10 shows a circuit 30b utilizing multiple LED arrays according to the invention. LED arrays 31a, 31b, 31c, each of which are substantially the same as the LED array 31 shown in FIG. 5, are coupled by their first terminals 33a-33c to the AC voltage source 34 through a common inductor L and are coupled by their second terminals 35a-35c through individual respective switches 36a-36c to the AC voltage source. Capacitors C_a, C_b, C_c are respectively coupled in parallel to each array 31a, 31b, 31c across their respective terminals. The circuit shown in FIG. 10 assumes that each array is operated in mutual exclusivity so that the L-C circuit as described above operates in the same manner in this circuit when each array is turned on. This type of circuit is well suited for a multiple display traffic signal. For example, if the LEDs in array 31a are all red light emitting, the LEDs in array 31b are all yellow light emitting, and the LEDs in array 31c are all green light emitting, the circuit is well suited for use in a red, yellow, and green traffic light where only one LED array is turned on at any given time. Using the circuit of FIG. 10, a single inductor L can be shared by all of the LED arrays, thereby reducing the cost of the traffic signal display unit.

There have been described and illustrated herein several embodiments of an AC powered LED array and circuits associated with it. While particular embodiments of the invention have been described, it is not intended that the invention be limited thereto, as it is intended that the invention be as broad in scope as the art will allow and that the specification be read likewise. Thus, while particular configurations have been disclosed in reference to a housing for the LED array and associated circuits, it will be appreciated that other configurations could be used as well. Furthermore, while the multiple array embodiment has been disclosed as having three arrays, it will be understood that different numbers of arrays can achieve the same or similar function as disclosed herein. Additionally, while the circuit of the invention has been described with reference to traffic signal displays, it will be appreciated that the circuit is useful in any AC powered illumination apparatus, including, but not limited to illuminated safety displays such as fire alarm

indicators, exit signs, airport and shipping displays, etc. It will therefore be appreciated by those skilled in the art that yet other modifications could be made to the provided invention without deviating from its spirit and scope as so claimed.

I claim:

1. An LED array circuit powered by a substantially sinusoidal AC voltage source of known frequency, comprising:

a) a first set of LEDs arranged as a first plurality of LED pairs, each LED pair comprising two oppositely polarized LEDs coupled to each other in parallel, and each LED pair being series connected to another LED pair, said first set of LEDs having first and second terminal nodes, coupled to and across the substantially sinusoidal AC voltage source;

b) an inductor arranged in series with and coupling one of said first and second terminal nodes to the substantially sinusoidal AC voltage source, said inductor having a 0 value and reactance chosen for improved power efficiency; and

c) a first capacitor coupled in parallel to said first set of LEDs at said first and second terminal nodes, wherein said inductor and said first capacitor form an impedance converter circuit which is tuned to the known frequency of the substantially sinusoidal AC voltage source thereby effectively converting the AC voltage source into substantially an AC current source, said substantially AC current source with said first set of LEDs providing said circuit with improved power efficiency.

2. A circuit according to claim 1, wherein:

said inductor has a Q greater than five.

3. A circuit according to claim 1, wherein:

said first set of LEDs comprises at least forty LEDs.

4. A circuit according to claim 1, wherein:

the values of said inductor and said first capacitor are chosen according to the approximate relationship $F = \frac{1}{2\pi\sqrt{LC}}$

$$F = \frac{1}{2\pi\sqrt{LC}}$$

where F is the frequency of the substantially sinusoidal AC voltage source, L is the value of said inductor, and C is the value of said first capacitor.

5. A circuit according to claim 1, further comprising:

d) a second plurality of LED pairs, each second plurality of LED pairs comprising two oppositely polarized LEDs, said second plurality of LED pairs being coupled to each other in series to form a second set of series connected LEDs having two terminal nodes coupled to and across the substantially sinusoidal AC voltage source, one of said two terminal nodes of the second plurality of series connected LEDs being coupled to the substantially sinusoidal AC voltage source by said inductor.

6. A circuit according to claim 5, further comprising:

e) a first switch means for coupling said first set of series connected LEDs to the substantially sinusoidal AC voltage source; and

f) a second switch means for coupling said second set of series connected LEDs to the substantially sinusoidal AC voltage source.

7. A circuit according to claim 1, further comprising:

d) a second plurality of LED pairs, each pair of said second plurality of LED pairs comprising two oppo-

- sitely polarized LEDs, said second plurality of LED pairs being coupled to each other in series to form a second set of series connected LEDs having two terminal nodes coupled to and across said substantially sinusoidal AC voltage source, with one of said terminal nodes being coupled by said inductor to the substantially sinusoidal AC voltage source; and
- e) a second capacitor coupled in parallel with said second set of series connected LEDs.
8. A circuit according to claim 7, further comprising:
- f) a first switch means for coupling said first set of series connected LEDs to the substantially sinusoidal AC voltage source; and
- g) a second switch means for coupling said second set of series connected LEDs to the substantially sinusoidal AC voltage source.
9. A circuit according to claim 1, wherein:
- said first set of series connected LEDs are mounted on one side of a substantially circular circuit board, and
- said substantially circular circuit board is attached to a substantially circular clear plastic disk with spacers such that said first plurality of series connected LEDs face said clear plastic disk.
10. An LED array circuit powered by a substantially sinusoidal AC voltage source of known frequency, comprising:
- a) a plurality of series connected LEDs having first and second terminal nodes, coupled to and across the substantially sinusoidal AC voltage source, said plurality of series connected LEDs comprising a plurality of LED pairs, each LED pair comprising two oppositely polarized LEDs coupled to each other in parallel, and each LED pair being series connected to another LED pair;
- b) an inductor arranged in series with and coupling one of said first and second terminal nodes to the substantially sinusoidal AC voltage source; and
- c) a first capacitor coupled in parallel to said plurality of series connected LEDs at said first and second terminal nodes, wherein said inductor and said first capacitor form an impedance converter circuit which is tuned to the known frequency of the substantially sinusoidal AC voltage source thereby effectively converting the AC voltage source into substantially an AC current source, wherein
- said plurality of series connected LEDs comprising a number of LEDs sufficient to cause a voltage drop across said first set of LEDs to be greater than a peak voltage of the AC voltage source.
11. A circuit according to claim 10, wherein:
- said inductor has a Q greater than five.
12. A circuit according to claim 10, wherein:
- the values of said inductor and said first capacitor are chosen according to the approximate relationship $F =$

$$\frac{1}{2\pi\sqrt{LC}},$$

where F is the frequency of the substantially sinusoidal AC voltage source, L is the value of said inductor, and C is the value of said first capacitor.

13. A circuit according to claim 1, wherein:
- said Q value is at least five.
14. An LED array circuit powered by a substantially sinusoidal AC voltage source of known frequency, comprising:

- a) a first set of LEDs arranged as a first plurality of LED pairs, each LED pair comprising two oppositely polarized LEDs coupled to each other in parallel, and each LED pair being series connected to another LED pair, said first set of LEDs having first and second terminal nodes, coupled to and across the substantially sinusoidal AC voltage source;
- b) an inductor arranged in series with and coupling one of said first and second terminal nodes to the substantially sinusoidal AC voltage source; and
- c) a first capacitor coupled in parallel to said first set of LEDs at said first and second terminal nodes, wherein said inductor and said first capacitor form an impedance converter circuit which is tuned to the known frequency of the substantially sinusoidal AC voltage source thereby effectively converting the AC voltage source into substantially an AC current source, said inductor having an inductance value chosen to provide a desired current for lighting said first set of LEDs, and said inductor chosen to have a high impedance while providing energy efficiency.
15. A circuit according to claim 14, further comprising:
- d) a second plurality of LED pairs, each second plurality of LED pairs comprising two oppositely polarized LEDs, said second plurality of LED pairs being coupled to each other in series to form a second set of series connected LEDs having two terminal nodes coupled to and across the substantially sinusoidal AC voltage source, one of said two terminal nodes of the second plurality of series connected LEDs being coupled to the substantially sinusoidal AC voltage source by said inductor.
16. A circuit according to claim 15, further comprising:
- e) a first switch means for coupling said first set of series connected LEDs to the substantially sinusoidal AC voltage source; and
- f) a second switch means for coupling said second set of series connected LEDs to the substantially sinusoidal AC voltage source.
17. A circuit according to claim 14, further comprising:
- d) a second plurality of LED pairs, each pair of said second plurality of LED pairs comprising two oppositely polarized LEDs, said second plurality of LED pairs being coupled to each other in series to form a second set of series connected LEDs having two terminal nodes coupled to and across said substantially sinusoidal AC voltage source, with one of said terminal nodes being coupled by said inductor to the substantially sinusoidal AC voltage source; and
- e) a second capacitor coupled in parallel with said second set of series connected LEDs.
18. A circuit according to claim 17, further comprising:
- f) a first switch means for coupling said first set of series connected LEDs to the substantially sinusoidal AC voltage source; and
- g) a second switch means for coupling said second set of series connected LEDs to the substantially sinusoidal AC voltage source.
19. A circuit according to claim 14, wherein:
- said first set of series connected LEDs are mounted on one side of a substantially circular circuit board, and
- said substantially circular circuit board is attached to a substantially circular clear plastic disk with spacers such that said first plurality of series connected LEDs face said clear plastic disk.

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20. An LED array circuit powered by a substantially sinusoidal AC voltage source, comprising:

- a) a first plurality of series connected LEDs having first and second terminal nodes, coupled to and across the substantially sinusoidal AC voltage source; and
- b) a current limiting inductor arranged in series with and coupling one of said first and second terminal nodes to the substantially sinusoidal AC voltage source, said inductor having an inductance value chosen to provide a desired current through said LEDs, and having a Q value and reactance for improved power efficiency.

21. A circuit according to claim 20, further comprising:

- c) a second plurality of series connected LEDs having two terminal nodes, one of said terminal nodes of said second plurality of LEDs being coupled to said first terminal node of said first plurality of LEDs, and the other of said terminal nodes of said second plurality of LEDs being coupled to said second of said terminal nodes of said first plurality of LEDs, such that said first plurality of LEDs are polarized in a first direction and said second plurality of LEDs are polarized in second direction opposite to said first direction.

22. A circuit according to claim 20, wherein:

said first plurality of series connected LEDs comprises a first plurality of LED pairs, each LED pair comprising two oppositely polarized LEDs coupled to each other in parallel, and each LED pair being series connected to another LED pair.

23. A circuit according to claim 22, wherein:

said inductor has a Q greater than five.

24. A circuit according to claim 20, wherein:

said inductor has a Q greater than five.

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25. A circuit according to claim 22, further comprising:

- c) a second plurality of LED pairs, each LED pair comprising two oppositely polarized LEDs, said second plurality of LED pairs being coupled to each other in series to form a second plurality of series connected LEDs having two terminal nodes coupled to and across the substantially sinusoidal AC voltage source, one of said two terminal nodes of the second plurality of series connected LEDs being coupled to the substantially sinusoidal AC voltage source by said inductor.

26. A circuit according to claim 25, further comprising:

- d) a first switch means for coupling said first plurality of series connected LEDs to the substantially sinusoidal AC voltage source; and

- e) a second switch means for coupling said second plurality of series connected LEDs to the substantially sinusoidal AC voltage source.

27. A circuit according to claim 26, further comprising:

- f) a first capacitor coupled in parallel to said first plurality of series connected LEDs at said first and second terminal nodes.

28. A circuit according to claim 20, further comprising:

- c) a first capacitor coupled in parallel to said first plurality of series connected LEDs at said first and second terminal nodes.

29. A circuit according to claim 20, wherein:

said first plurality of series connected LEDs are mounted on one side of a substantially circular circuit board, and said substantially circular circuit board is attached to a substantially circular clear plastic disk with spacers such that said first plurality of series connected LEDs face said clear plastic disk.

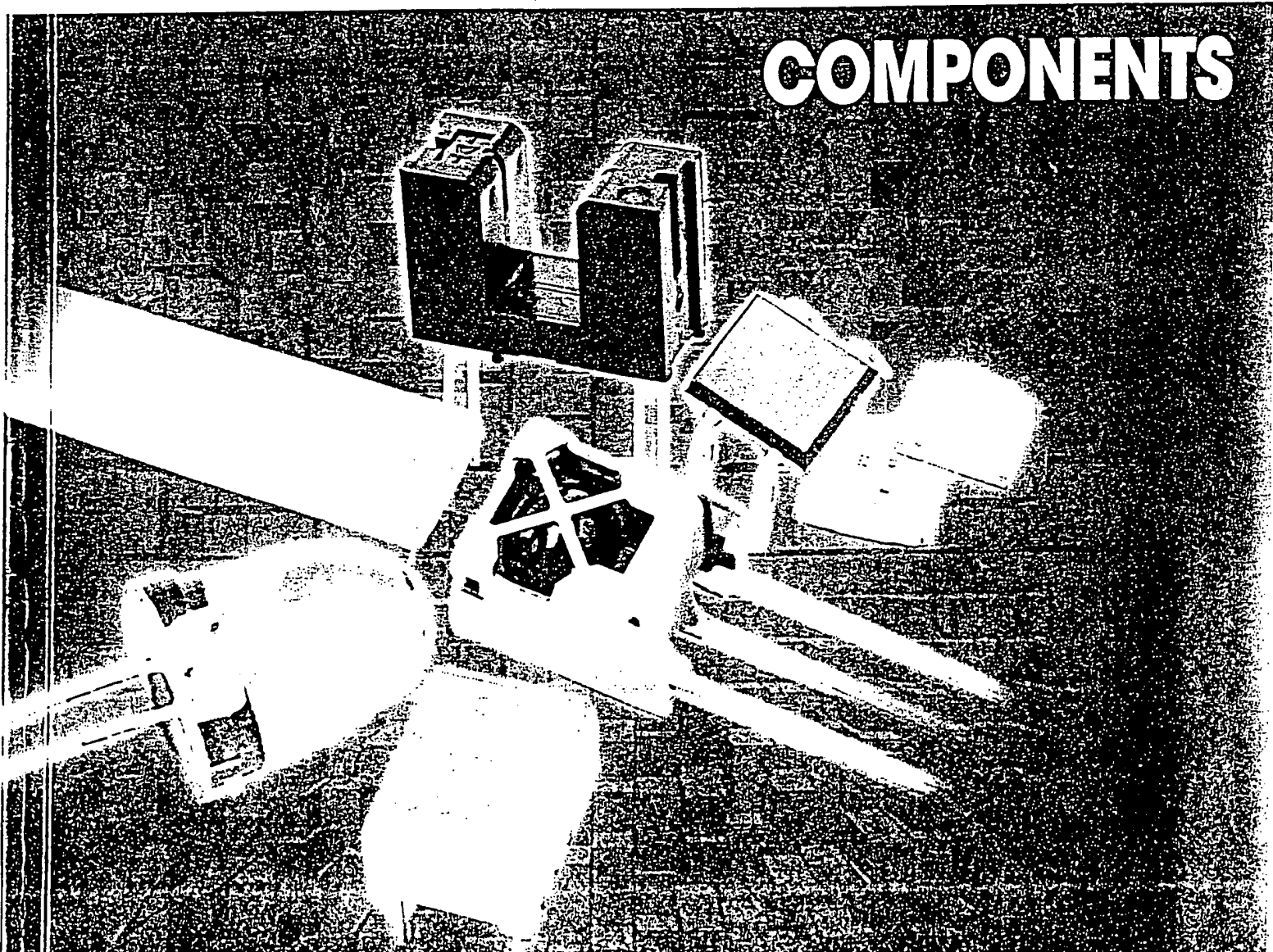
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1999 ^B
SHORT FORM

OPTO-ELECTRONIC

EVERLIGHT

COMPONENTS

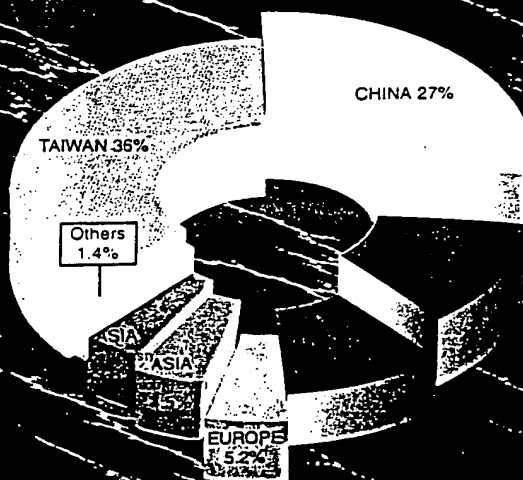
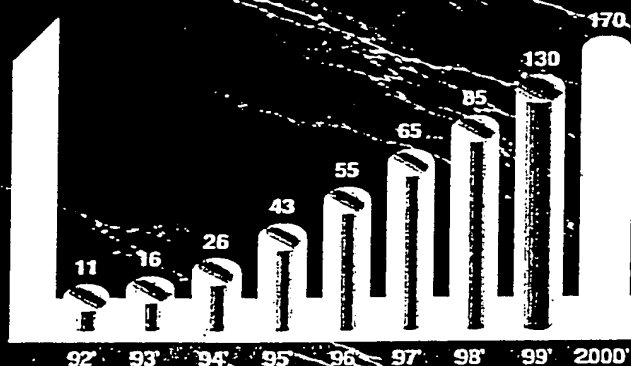


History in Brief

- **1983**
Company established to produce LED Lamps.
- **1984**
Started to produce invisible products (PT/IR/PD)
- **1989**
Yue-Lee, Miao-Li Factory was built up
- **1990**
Selected by SHARP, Japan as the only OEM collaborator in Taiwan
- **1991**
Pan Yu Factory in Mainland China established
- **1991**
Moved to new a building in Tucheng (floor size:11,570m²)
- **1993**
Successfully developed Light Source, IRM and Big Lamp
- **1994**
Selected by Quality Technologies (QT) as the OEM collaborator in Taiwan
- **1995**
ISO-9002 certification
- **1996**
ISO-9001 certification
- **1996**
Successfully developed complete SMD series and became a main supplier of diversified mini LED of the world
- **1997**
Successfully developed LASER Diode, mini IRM, PSD, IrDA and Hall IC
- **1997**
Applied QS-9000, ISO-14000 to enter into automobile and aviation parts market.
- **1998**
Company traded in OTC market
- **1998**
Successfully developed WHITE Lamps & relative products
- **1998**
QS-9000 certification
- **1998**
To purchase land (23,140m²) in Lung Tang, Taiwan for factory expansion
- **1998**
To Purchase land (120,000m²) in Guangzhou for factory expansion
- **1998**
Applied for public listing company in TAIEX
- **2000**
Estimated to attain an annual sales of 5.5 billion NT dollars

Sales Billing

(U.S. million dollars)



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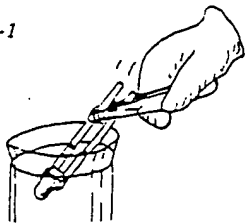
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Generally, the LED can be used the same way as other general purposed semiconductors. However, the following precautions must be taken to protect the LED

► CLEANING

Don't use unspecified chemical liquids to clean the LED, they could harm the resin of the LED. If cleaning is necessary, please immerse the LED in alcohol or Freon TE at normal temperature for less than one minute. When other chemical solutions not specified are used, it may cause cracks or haze on the surfaces of the lens.(Fig-1)

Fig-1



► FORMING

1. Don't form the leads during or after soldering. If forming is required, it must be done before soldering.
2. Please remember, any pressure applied on resin can break gold wire in LED.
3. Form pin leads by securing under the tie bar cut(Fig-2-1), and bending with radio pliers, or the equivalent to avoid pressure on resin.(Fig-2-2)

Fig-2-1

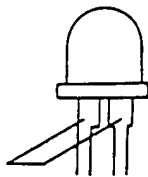


Fig-2-2

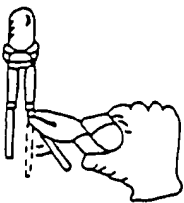


Fig-3



► SOLDERING

1. Solder under the tie bar cut(Fig-4). Hold pin leads with tweezers during soldering, especially for smaller LEDs.

Fig-4

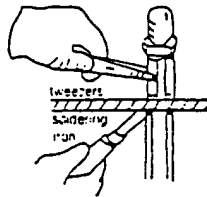
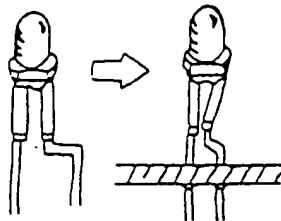
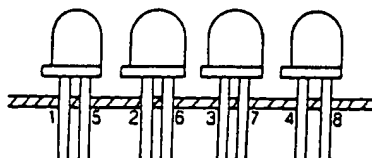


Fig-5

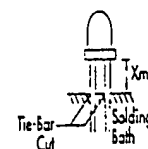
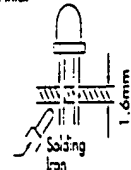


2. If pressure is applied on LED while it is being on P.C. board, disconnection may occur during soldering or after mounting due to creep. Pin lead mounting holes must be coincided with original or formed pin lead pitch to prevent pressures.
3. During lead forming process should not be added any stress to the LED, otherwise fractures will be happened. The device epoxy and possibly break bond wires, which will cause failure.
4. When an LED is mounted into a P.C. board, pitch spacing should be aligned carefully to avoid causing any stress to the lead wires. Otherwise the stress will cause problem in high temperature operation. It is necessary for the LED to return to normal temperature in three minutes after the soldering operation.(Fig-5)
5. If soldering one line of LEDs on a P.C. board by using a soldering iron, don't solder both leads of the LED at the same time.(Fig-6)

Fig-6



6. The soldering iron should be operated under 30W power consumption.
7. The LED soldering specification is shown as below:

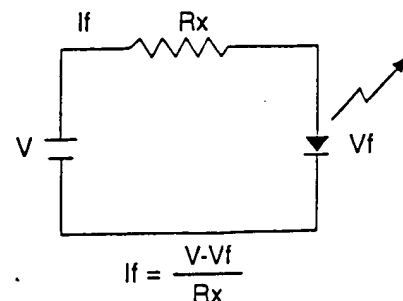
Method	Conditions	Temp.	Time
Soldering bath method	Dip LED up to Xmm from Resin 	230°C±5	Within 3 Seconds
Soldering method	Soldering iron: 30W Tip: 4.5x32mm Through hole P.C.B. 1.6mm thick 	Tip Temp. 295°C±5°C	Within 3 Seconds

► PREVENTING OVERCURRENT.

1. Do not overcurrent.
2. In order to operate the LED under stable conditions, put protective resistors in series. Resistor Values can be determined by supply voltage or current for the LED. Recommended current for use is in the range of If 10mA.(Fig-7)
3. Circuit must be designed so that overvoltage (overcurrent) is not applied to the LED during ON/OFF switching. Transients or pulse current can damage the junction of the LED die.

► BRIGHTNESS AND COLOR

1. For obtaining more brightness, multiple LEDs should be kept at the same current.
2. Increase current to increase brightness.
3. Check defects at a distance of 30cm from the LED to the eye.
4. Use on If 20mA If possible to obtain the most uniform brightness on yellow and green LEDs.



■ RI

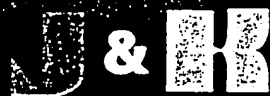
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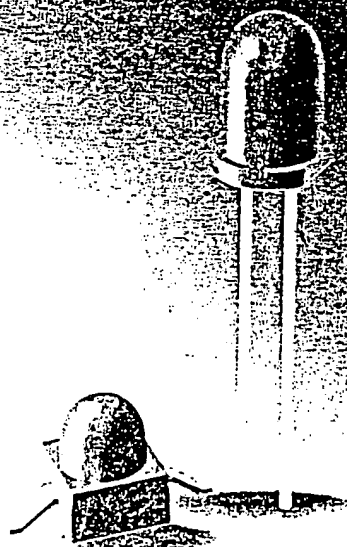
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SUPER BRIGHT INDUSTRIAL LTD.

www.led.com.hk



LED LAMPS

SUPER BRIGHT LED

ULTRA BRIGHT LED

AXIAL TYPE LED

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BRIEF INTRODUCTION



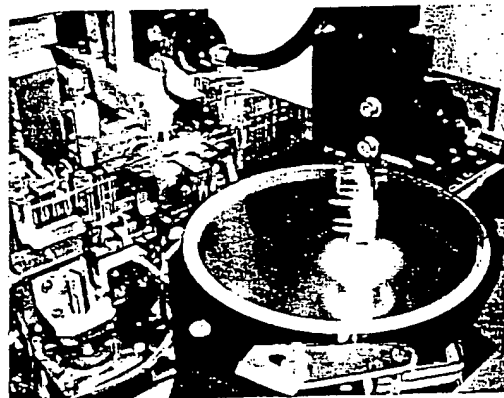
另中文版請閱背面

Super Bright Industrial Limited was established in 1986. We have two factories in Mainland China which are located in Guangdong has obtained ISO9002 Certification. Totalling 3000 sq. meter and employing about 1000 workers. Our monthly output is about 50 million pieces and is increasing steadily. We provide a wide range of products such as :



- LED Lamps
- Super Bright / Ultra Bright LED
- Axial LED, SMD
- Cluster LED
- Infra Red Emitters
- Photo Diodes, Photo Transistor
- Photo Interrupter
- Digit Display, Dot Matrix & Backlight.
- We also provide OEM & assembly aimed at custom design.

In order to improve the quality and reliability of our products, our R & D Division keep researching for new technologies and materials, such that Led Chips and other major materials are mainly imported from Taiwan & USA. Besides, we are using Automatic Die Bonder to facilitate production. To satisfy user's needs for high quality products, all of our products are checked by Fully Auto Test & Classification Machine. Molding parts are produced in house by modern machinery and facility. Thus, the fully control of manufacturing process is achieved.



Our company's common objectives towards our valuable customers are:

- To provide good services and technological consultation.
- To provide high quality of products.
- To provide efficiency delivery.
- To strengthen the role of the cost control function in order to make our products becomes more competitive.

To following the footstep of the 21st Century's advanced technology. Therefore, the more we grow, the more we can contribute to our customers.

Agent / 總代理

Rm. 910, Kwong Sang Hong Ctr., 151 Hoi Bun Rd.,
Kwun Tong, Kowloon, Hong Kong.

九龍觀塘海濱道 151-153 號廣生行中心 9 樓 10 室

E-mail: superbright@led.com.hk Site: www.led.com.hk

Tel: (852) 27711665 Fax: (852) 2783 9543

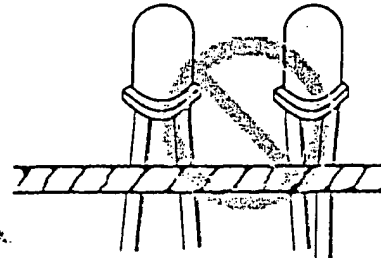
SUPER BRIGHT INDUSTRIAL LTD.

□ CLEANING

Don't use unspecified chemical liquid because it may cause cracks or haze on the surface of lens.
"Use Alcohol, Freon TE or Chlorosen to clean LED"
at room temperature for less than 1 minute.

□ FORMING

"If forming is required, it must be done before soldering." Form pin leads by securing under 5mm from body and bedding with radio pliers or the equivalent to avoid pressure on resin.
"When the LED is mounted into a P.C. board, pitch" spacing should be aligned to prevent cause any stress to the resin.
Any unsuitable stress applied to resin may break "bonding wire in LED, which will cause failure."

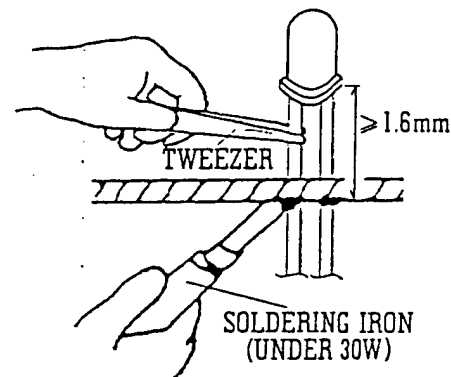


□ SOLDERING (AUTOMATIC SOLDERING)

- Check and keep record of all soldering equipment's temperature .
- Pre-heating : 100°C max. (Resin surface temperature) 60 sec. Max
- Each soldering point must not exceed 5 second with 260°C
- Prevent any shock or vibration excise on the PCB shortly after soldering otherwise the bonding wire inside the led will be broken

(HAND SOLDERING)

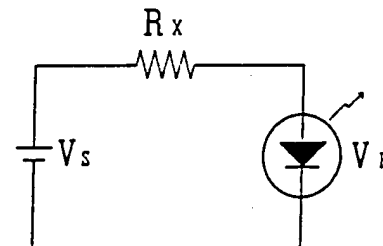
- It is suggested that using a fixture for soldering is needed during hand soldering to prevent heat transfer to the dice.
- Temperature at tip of iron: 300°C max. (30 W max.)
soldering time: 3 sec. max. Location: At least 3mm away from resin body
- Making sure of pitch of PCB and LED leads are the same, otherwise there might have strength transfer during insertion and causing damage to the gold wire when soldering.



□ PREVENTING OVERCURRENT

Apply overcurrent may cause LED failure or reduce life and brightness.
"Put protective resistor in series, not only prevent"
"overcurrent, but also keep LED in uniform" brightness.
Resistor value can be determined by the formula.

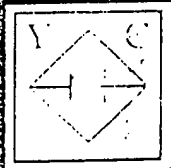
$$R_x = \frac{V_s - V_f}{I_f}$$



□ BRIGHTNESS

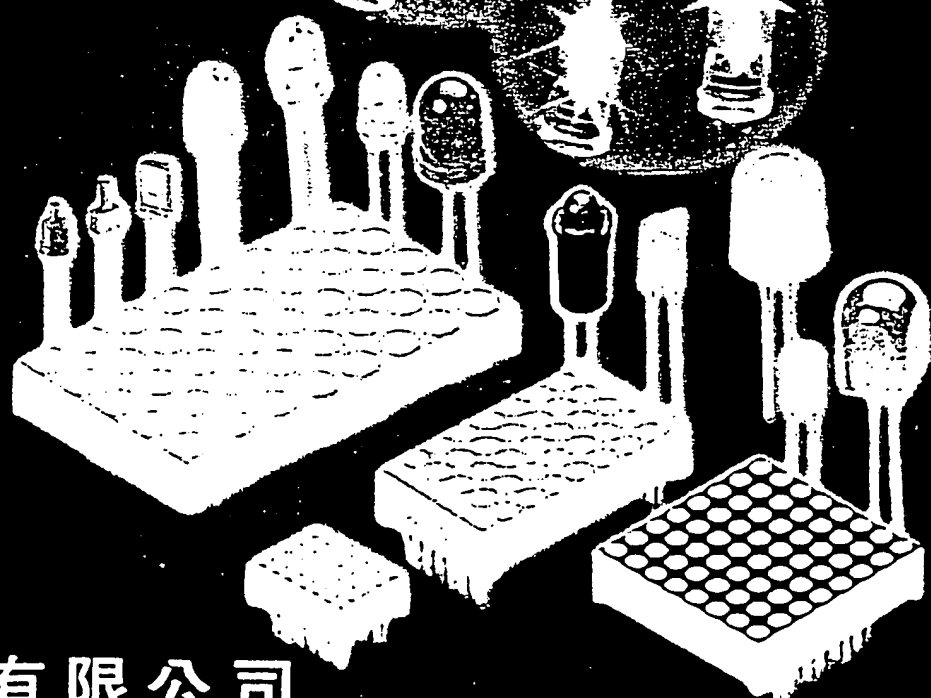
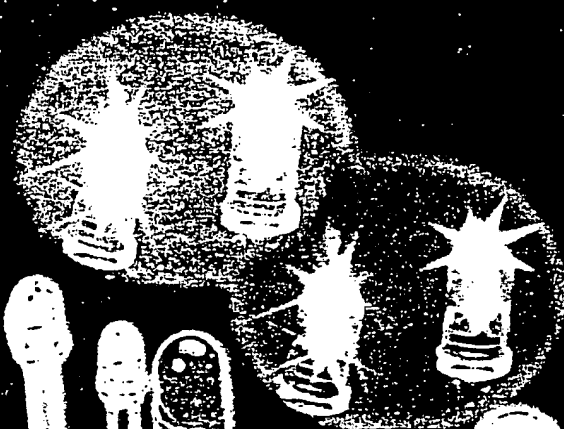
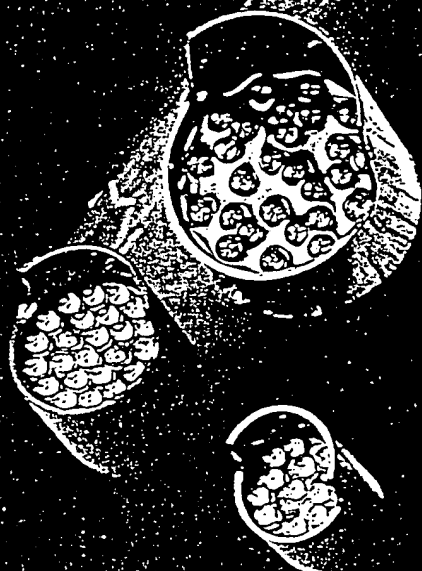
"For the purpose of obtaining uniform brightness," LED shall be kept at the same current but not voltage.
It is useful for uniform bright if use larger source voltage and protective resistor.
Use on forward current 20mA to obtain the most uniform brightness on yellow and green LEDs.

NOTE: CHECK AT A DISTANCE OF 30cm FROM THE LED TO THE EYE DEFECTS.



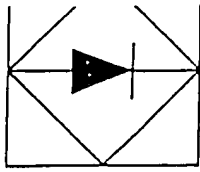
YIOW CHIE

DISPLAY



有及實業有限公司

YIOW CHIE INDUSTRIAL CO. LTD.



COMPANY PROFILE

YIOW CHIE INDUSTRIAL CO., LTD.

YIOW CHIE industrial co., ltd. was established in 1986, a specialist manufacturer of light emitted diode such as Led Lamps, Seven Segment Displays, Dot matrix, LED Light Bar and SMD.

Since the establishment of YIOW CHIE INDUSTRIAL CO., LTD. 15 years ago, under integrated efforts of our knowledgeable and experienced staff and managers. We have continued to develop the most advanced reliable products for our clients, hence we have contributed eminently to the electronic assembling industry.

For the past years with our customers patronage and support, YIOW CHIE has gained tremendous growth, and we are proud of our continuous offer of quality products to leading domestic and foreign electronic firms.

As we approach the year 2000, the electronics industry has progressed dramatically through the rapid development of new material technology. And in response to the evolving needs of the ear, we strive for complete office and engineering automation through the introduction of modern equipments and most recent R&D.

With dedication, YIOW CHIE pursuits for a progressive high-tech environment which serves man and society in an exciting and unprecedented way.

With all efforts, we will commit ourselves to our domestic and international clients. We thank you for your continuous support and guidance and your patronage is always welcomed.

The yield of leds lamp is 20KK pcs and 500kpcs for led display monthly.

Please contact us through:

OFFICE: Yiow Chie Industrial Co., Ltd.

10Fl, No. 120-11, Sec. 3, Chung-San Rd., Chung Ho City, Taipei, Taiwan, R.O.C.

TEL : 886-2-2221-3358

FAX : 886-2-2221-3821

E-MAIL : yiowchie@ms19.hinet.net

yiowchie@yiowchie.com.tw

WEBSITE : <http://www.yiowchie.com.tw/>

HONG KONG OFFICE: Skytruth International Co., Ltd.

Rm., 4, 10/F, Yale Industrial Centre, 61-63, Au Pui Wan Street, Fo Tan, Shatin, N.T.

Hong Kong

TEL: 852 2605 7432

FAX: 852 2694 9990

FACTORY: Shanghai Jialong Electronic Co., Ltd.

No. 511, Jialow Highway, Jiading, Shanghai

TEL: 86 21 5954 9613 . 5954 9654

FAX: 86 21 5954 9288

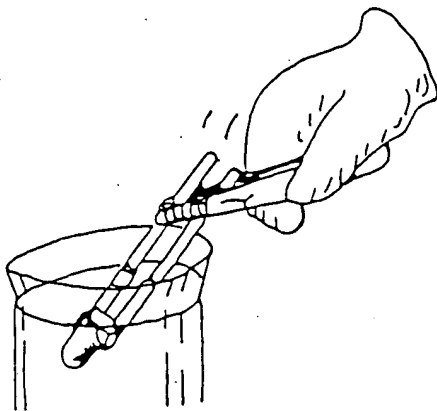
HOW TO USE THE LED

Generally, the LED can be used the same way as other general purpose semiconductors. However, the following precautions must be taken to protect the LED.

■ CLEANING

Don't use unspecified chemical liquids to clean the LED, they could harm the resin of the LED. If cleaning is necessary, please immerse the LED in alcohol or Freon TE at normal temperature for less than one minute. When other chemical solutions not specified are used, may cause cracks or haze on the surfaces of the lens. (Fig. 1)

Fig-1



■ FORMING

1. Don't form during or after soldering. If forming is required it must be done before soldering.
2. Please remember, any pressure applied on resin can break gold wire in LED.
3. Form Pin by Leads securing under the tie bar cut (Fig. 2), and beddig with radio pliers or the equivalent to avoid pressure on resin. (Fig. 2)

Fig-2

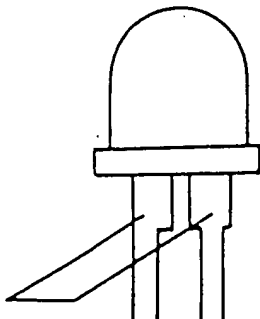


Fig-2

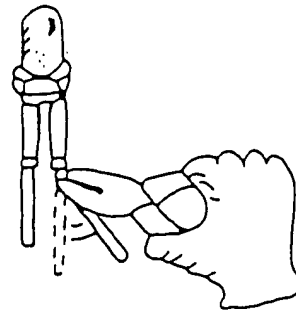


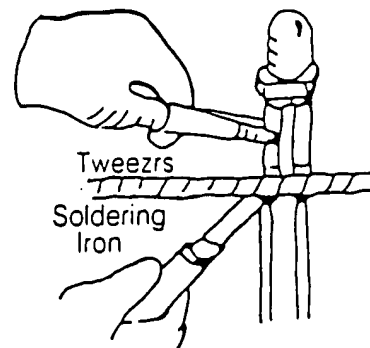
Fig-3



■ SOLDERING

1. Solder under the tie bar cut (Fig. 4). Hold pin Leads with tweezers during soldering, especially for smaller LEDs.

Fig-4

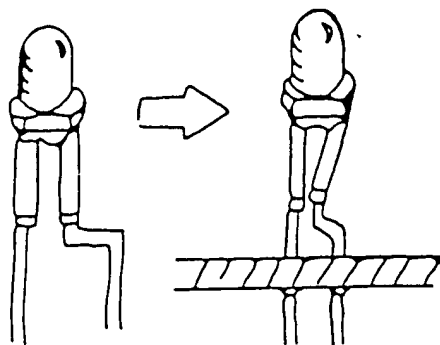


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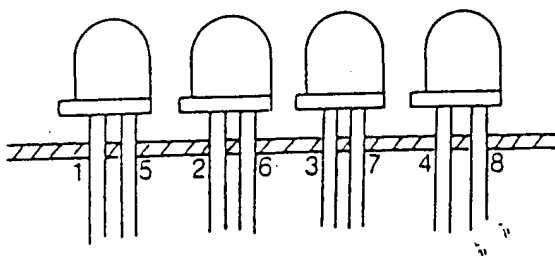
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Fig-5

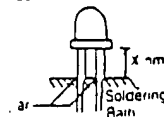
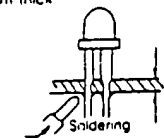


2. If pressure is applied on LED while it is being on P.C. board, disconnection may occur during soldering or after mounting due to creep. Pin Lead mounting holes must be coincided with original or formed pin Lead pitch to prevent pressures.
3. During Lead forming process should not be added any stress to the LED, otherwise fractures will be happened. The device epoxy and possibly break bond wires, which will cause failure.
4. When an LED is mounted into a P.C. board, pitch spacing should be aligned carefully to prevent cause any stress to the Lead wires. Otherwise the stress will cause problems in high temperature operation. Three minutes are necessary for the LED to return to normal temperature after the soldering operation. (Fig. 5)
5. If soldering one line of LEDs on a P.C. board by using a soldering iron, don't solder both the Leads of the LED at the same time. (Fig. 6)

Fig-6



6. The soldering iron should be operated at under 30W power consumption.
7. The LED soldering specification is shown as below:

Method	Conditions	Temp	Time
Soldering bath method	Dip LED up to Xmm from Resin 	$230^{\circ}\text{C} \pm 5$	Within 3 Seconds
Soldering method	Soldering iron 30W Tip 4.5ø x 32mm Through hole P.C.B 1.6mm thick 	Tip Temp $295^{\circ}\text{C} \pm 5^{\circ}\text{C}$	Within 3 Seconds

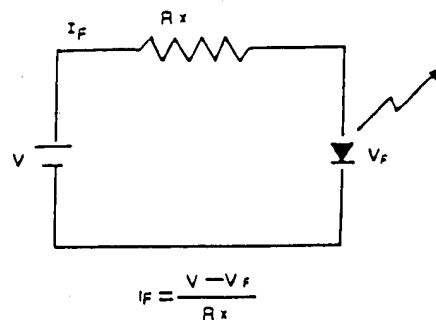
■ PREVENTING OVERCURRENT.

1. Do not overcurrent.
2. In order to operate the LED under stable conditions. Put protective resistors in series. Resistor Values can be determined by supply Voltage or current for the LED. Recommended current for use is in the range of I_F 10mA to 20mA. (Fig. 7)
3. Circuit must be designed so that overvoltage (overcurrent) is not applied to the LED during ON/OFF switching. Transients or pulse current; can damage the junction of the LED die.

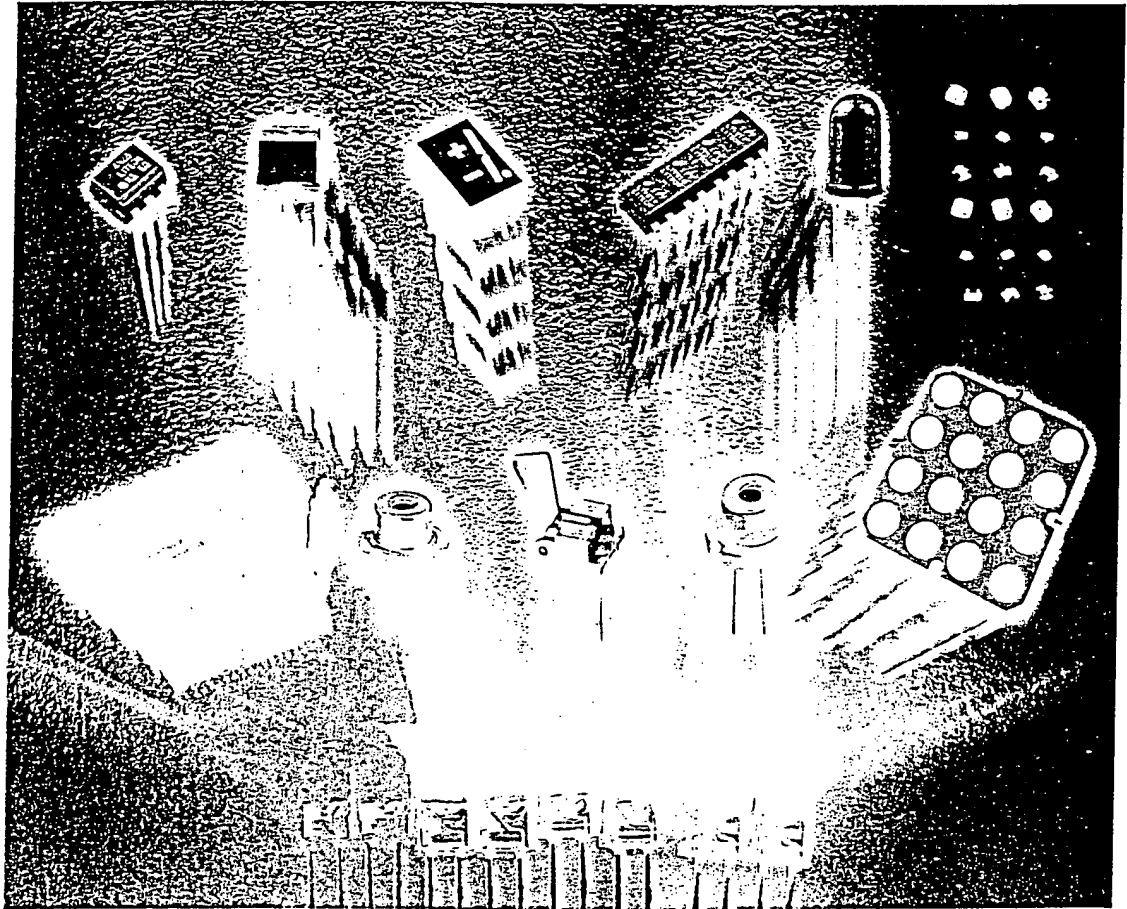
■ BRIGHTNESS AND COLOR

1. For obtaining even brightness multiple LEDs should be kept at the same current.
2. Increase current to increase brightness.
3. Check at a distance of 30cm from the LED to the eye defects.
4. Use on I_F 20mA if possible to obtain the most uniform brightness on yellow and green LEDs.

Fig-7



LITEON



Optoelectronics

SHORT FORM
1998 - 1999

As one of the world's largest independent manufactures of optoelectronic products, LITE-ON offers a wide range of reliable and economical optoelectronic design solutions. LITE-ON is the first IECQ - certified opto products manufacturer in the world , also qualified for ISO 9000 & ISO 14000 certification.

We produce high quality LEDs , single , dual and multi-digit displays , visible and infrared discrete components, customer display modules and over 40% of the world's clock displays.

Our fully automated production facilities in Taiwan , Thailand and Tianjin also features the very latest in production equipment - much of which was designed by LITE-ON engineers - and provides the most comprehensive reliability test facilities in the industry.

The LITE-ON product line is backed by a team of experienced support specialists. So you can be assured that your problems and questions will be met with professionalism and expediency.

To order any component in this SHORT FORM or additional applications information, call the LITE-ON office nearest you. Please refer to address list.

We reserve the right to amend any specifications / information without prior notice.

Editor

LITE-ON ELECTRONICS, INC.

Address: 90, Chien I Road, Chung Ho, Taipei Hsien, Taiwan, R.O.C.

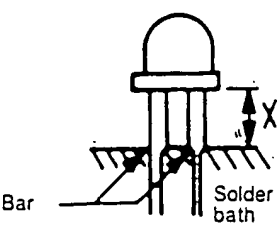
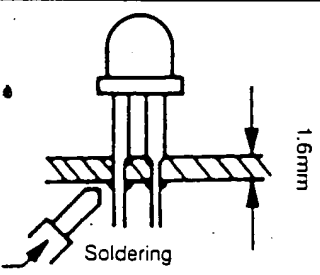
TEL : +886-2-22226181

FAX : +886-2-22210660 / 22256846

How To Use LITE-ON LED Lamps

3.6 The soldering iron should be operated at under 30w power consumption.

3.7 The LITE-ON LED soldering specification is shown as below:

Method	Conditions	Temp	Time
Soldering bath method	Dip LED up to Xmm from resin  A diagram showing an LED package being dipped into a solder bath. A dimension line indicates the depth 'X' from the resin body to the top of the leads.	$230^{\circ}\text{C} \pm 5$	Within 5 Seconds
Soldering method	Soldering iron: 30W Tip: $4.5 \phi \times 32\text{mm}$ Through hole P.C.B. 1.6mm thick  A diagram showing a 30W soldering iron with a specific tip being used to solder the leads of an LED package through a 1.6mm thick PCB. The word 'Soldering' is written near the iron tip.	Tip Temp $295^{\circ}\text{C} \pm 5^{\circ}\text{C}$	Within 3 Seconds

4. Preventing Overcurrent

4.1 Do not overcurrent.

4.2 In order to operate LITE-ON LED under stable conditions, put protective resistors in series, Resistor Values can be determined by supply voltage or current for the LED. Recommended current for use is in the range of IF 10mA to 20mA. (fig.7)

4.3 Circuit must be designed so that overvoltage (overcurrent) is not applied to the LED during ON/OFF switching. Transients or pulse current can damage the junction of the LED die.

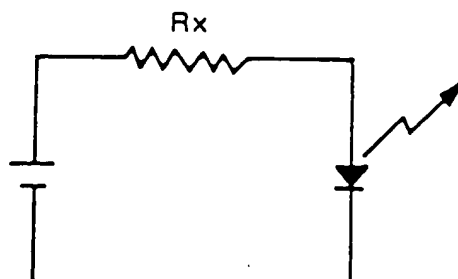
5. Brightness and Color

5.1 For obtain even brightness multiple LED should be kept at the same current.

5.2 To increase brightness, increase current.

5.3 It checked at a distance of 30cm from the LED to the eye detects.

FIG. 7



Duane J. Knize
5572 Ladybird Ln
La Jolla, California 92037
September 28, 2000

Mark Allen
Fiber Optics Designs
Research and Development Office
7601 Eads Avenue, Suite 8
La Jolla, California 92037

Dear Mr. Allen,

I was asked to comment on patent 5,941,626 regarding the requirement for, and use of, a resistor in the LED circuits described in the patent and to disclose my qualifications to make these comments.

Background Information: I received Bachelor of Science and Master of Science degrees in electrical engineering from Harvey Mudd College, Claremont California, in 1973. I have over 27 years of engineering experience including circuit and electronic system design and development. I am currently Chief Scientist for the Technology Research Group at Science Applications International Corporation. SAIC is a large engineering service firm with revenues over \$4B/year, supporting government and commercial clients. TRG is, as the name implies, the premier technology R&D organization in the company.

Comments on Patent 5,941,626: First of all, all the art showing circuit detail includes a resistor. Specifically, figure 1A, showing the circuit configuration, and figure 1B, showing the circuit schematic of the first embodiment of the invention include a resistor. No detailed configuration or schematic is provided for the second embodiment. Although it is mechanically different, it uses a circuit identical to that of the first embodiment (as explicitly stated in column 6, lines 34-36). Figures 10A and 10B provide the circuit configuration the circuit schematic of the third embodiment of the invention. Both include a resistor.

Secondly, the discussion contained in column 3 lines 30-40 clearly promulgates a requirement for this resistor. The wording stresses the fact that one might not think that a resistor is required ("Since the required power source of 100V is equal to the current (sic) source voltage in Japan, the resistance 8 apparently seems unnecessary."), but in fact it is ("However, it is proved from experience that the apparatus is stable in function by providing the resistance 8.").

Finally, nowhere in the patent is any reference or allusion to a version of the apparatus operating without a stabilizing resistor.

It seems clear to me that the author of patent 5,941,626 considered the resistor to be an essential part of his invention.

If you have any questions, please don't hesitate to call me during business hours at 858-826-6544.

Best regards,

A handwritten signature in black ink, appearing to read "Duane J. Knize", with a stylized, flowing script.

Duane J. Knize

LETTER OF TESTIMONY

I, the undersigned, am an independent consultant being paid by Fiber Optic Designs Inc., to provide an unprejudiced, expert opinion as to the teaching and interpretation of U.S. Patent 5,941,626 to Yamuro entitled, "Long Light Emitting Apparatus." Specifically, I have been asked how I might interpret Yamuro's teaching on the use or omission of a resistor in his circuitry. Other than a supplied text, Basic Electronics by Bernard Grob, I have not been provided with any further information or instruction on the subject. Other than to perform the above task, I have no connection or affiliation with Fiber Optic Designs whatsoever.

I possess a degree in Political Science and have been long working as a consultant to various governments and private parties, primarily on environmental issues. From time to time I also have given expert witness or testimony, provided I felt capable of doing so. I am not trained in electrical engineering and therefore I was at first reluctant to provide this testimony. However, after reading the subject material, I consider it simple enough to understand and fully interpret. With my background as an author and editor for several technical journals, I feel confident in my ability to judge and interpret most technical writing, provided I can understand it. To me, the subject material leaves clear conclusion, without doubt, as to its meaning. My interpretation is as follows:

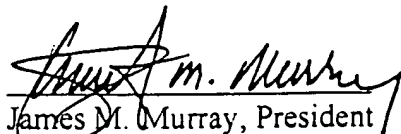
The resistor is used throughout the subject material. The only reference to any possible questioning of use of the resistor is in column 3, lines, 30-40. In column 3, lines 30-33, the author states, "*Therefore, the power source required for each light emitting unit 6 is 100 V. Since the required power source of 100 V is equal to the common source voltage in Japan, the resistance 8 apparently seems unnecessary.*" This comment leaves an impression that the resistor might be unnecessary in this case because the voltage used in the power source is equal to the voltage required by each light emitting unit. From a fundamental understanding that resistors would use up some of this voltage, if the resistor was kept in the circuit, the voltage to the light emitting unit would be less.

However, this notion of possibly removing the resistor is disproved by the next statement that immediately follows in lines 34-37, "*However, it is proved from experience that the apparatus is stable in function by providing the resistance 8. Therefore, the resistance 8 is connected to the circuit shown in FIGS. 1A and 1B.*" Here, the author qualifies, using experience as the reason for proof, that the resistor provides stability by using the resistance, making the resistor needed. The author teaches that the resistor is required for the device to be stable in function. The author implies from this that an unstable device would result if the resistor were possibly removed. The author shows use of the resistor for the rest of the subject matter that follows.

Finally, in lines 37-40, the author describes how the resistor value may be determined for another case example, *"50 or less LED lamps 4, for example 45 or 40 LED lamps, can be connected to the light emitting unit 6. In this case, the resistance value corresponding to the potential difference from the power source 9 is set as the resistance 8."* Here the author states that up to 50 LED lamps may be used in the device. This implies that the use of more than 50 LED lamps is incorrect. The author then describes a case where a typical number of LED lamps would be used: 45 or 40 LED lamps. For this case, the author describes how the resistor value is determined. The computation adds the voltages of the LED lamps (e.g., 45 times 2 V or 40 times 2 V) and subtracts this sum from the voltage of the source (100 V). It is easy to see that the resistor would be determined from either 10 V or 20 V, depending on whether 45 or 40 LED lamps, respectively, were used. The case of 50 LEDs is unclear by this method, implying to me that author allows other methods to determine the resistor, and that this method is but one example.

The author's overall teaching may be summarized as follows. First, the author uses an initial case (50 LED lamps) to prove that, in all cases, a resistor must be used in this device. Second, the author teaches that a resistor is always used in his device, justifying device stability. Third, the author teaches the upper bound on the number of LED lamps to use (50 lamps). Fourth, the author gives typical examples encountered for the number of LED lamps to use (45 or 40 lamps). Finally, the author teaches a method to determine the resistor for these examples (using 10V or 20 V).

It seems obvious to me that the author justifies need for, and use of, a resistor in a circuit containing only LED lamps. In explaining the need for, and use of, the resistor in the circuit, the author cites experience as the basis of reasoning, indicating that the procedure is well known. The author teaches that it is necessary to use the resistor for the circuit to operate correctly. The author also teaches the number of LED lamps to use in the circuit, and a method of finding the resistor value for some typical examples.


James M. Murray, President
Timberock USA Company
P.O. Box 2973
La Jolla, CA 92038

9721 Chapel Road
Philadelphia, PA 19115-2528
1 October 2000

Mr. David R. Allen
President
Fiber Optic Designs, Inc.
704 Floral Lane Blvd.
Yardley, PA 19067

Dear Mr. Allen:

At your request, I have reviewed two documents and evaluated specific statements in them for consistency.

The two documents are:

- United States Patent No. 5,941,626 (24 August 1999) : Yamuro – Long Light Emitting Apparatus
- U.S. Patent Office Action, dated 22 August 2000, relative to Mr. Mark Allen's Application, Serial Number 09/339,616

Under consideration is whether Mr. Yamuro's patent permits use of his apparatus without resistors, a fundamental claim in Fiber Optic Designs' patent application for a similar apparatus.

The following appears in the section titled, "Description of the Preferred Embodiments" of Mr. Yomuro's patent. All underlinings are mine:

"The conductor is connected to one terminal of a power source plug whereas the conductor is connected to the other terminal of the power plug through a resistance..."

"Since the required power source of 100V is equal to the common source voltage in Japan, the resistance apparently seems unnecessary. However, it is proved from experience that the apparatus is stable in function by providing the resistance."

In her rejection of Mr. Allen's request for reconsideration, U.S. Patent Office Examiner Vo noted:

"Even though this figure shows one end of the diode block being tied to the source via the resistor, by its natural layout, it fulfils applicant's definition of having this block directly ties (sic) to the source..."

"Even though Figure 1B shows the usage of a resistor to stabilize the operation of the system, it's (sic) teaching, however, specifically leaves the option of using this resistor to one of ordinary skill in the art..."

"Applying the design without the resistor as suggested in a massive production environment, this would mount up to a considerable saving in the production line."

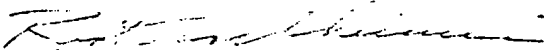
A review of Mr. Yamuro's patent discloses no circuit diagram that does *not* include resistance. His statements concerning the need for resistors in his apparatus are highly qualified, as indicated in the underlined sections, above.

Nothing in the granted patent, other than the claim that resistance "apparently seems unnecessary," describes or shows the apparatus *without* the use of resistance. Further, the patent affirms that experience has shown the patented apparatus to be stable only when resistance is provided. *There is a clear implication that the patented apparatus would be unstable without the resistance.* It could therefore be argued that Mr. Yamuro's apparatus functions predictably only with the use of resistors, and that there is no proven basis for the claim that it can be used, as well, without them.

Ms. Vo's assertion, that Mr. Yamuro's apparatus would operate stably by using "ordinary skill in the art" instead of resistors, should be challenged. This is an unsatisfactory generalization that Ms. Vo, or Primary Examiner David Vu, should be asked to define specifically. Absent a satisfactory definition, you may want to pursue your application for a patent on a light-emitting apparatus that, though in some ways similar to Mr. Yomura's, differs specifically by permitting stable operation without resistors.

You may use this letter in any way you see fit. Should you decide to copy or forward it, readers should know that I am neither a lawyer nor an engineer. I was, however, editor-in-chief of the *AT&T Technical Journal* at Bell Laboratories for five years, and am now retired.

Sincerely,



Bert Vorchheimer

TRANSCRIPT OF VIDEOTAPE EXPERIMENTS
IN SUPPORT OF APPLICANT'S RESPONSE TO
OFFICE ACTION DATED 8/29/00

"Preferred Embodiment to

LED Light String"

Regardfuly

U.S. CIP Patent Application

Serial Number 09/339,616

Video Demonstration

DAVID ALLEN, President, Fiber Optics Designs, Inc.

The purpose of this videotape is to present real time experiments which address the following issues:

1. Test the applicant's theory that an LED circuit that adds the DC specified voltage of each LED to match the AC power source requires a resistor.

2. Test the examiner's theory that (a) Yamuro U.S. Patent 5942626 suggests that the resistor is optional in the circuit; (b) that the circuit is operably stable without the resistor. The experiments presented duplicate both the Yamuro circuit and the substitution of DC value LEDs for a resistor in an AC powered circuit. In all cases, the circuits are unstable and fail quickly. Thank you for your attention.

EXPERIMENT #1

PURPOSE:

Test the stability of the Yamuro circuit (resistor included) and the Examiner's Option circuit [substitute LEDs for the resistor such that the sum of the LED+DC specified voltage = AC source voltage of 110VAC]

METHOD:

Duplicate the Yamuro circuit by building an equivalent circuit using U.S. AC source voltage... Experimental circuit is identical

to Yamuro's circuit since $50\text{ VDC LEDs} + 500\text{ Ohm resistor} @ 170\text{ VAC (experiment)} = 45\text{ VDC LEDs} + 500\text{ Ohm resistor} @ 100\text{ VAC (Yamuro Example)}$.

LEDs Used (Manufacturer's Specifications):

2.0 μ f DC at 20mA (typical)

**2.4 μ f DC at 20mA (maximum),
Resistor Used: 500 Ohms**

10 Volts/0.02 Amps (typical) = 500 Ohms

**Note: It is known that LED voltage is
specified under DC operating conditions.**

CINCOY DESCRIPTION

THE OLD CAMBODIAN



ИЗДАНИЕ

התאחדות

三

100% Cotton

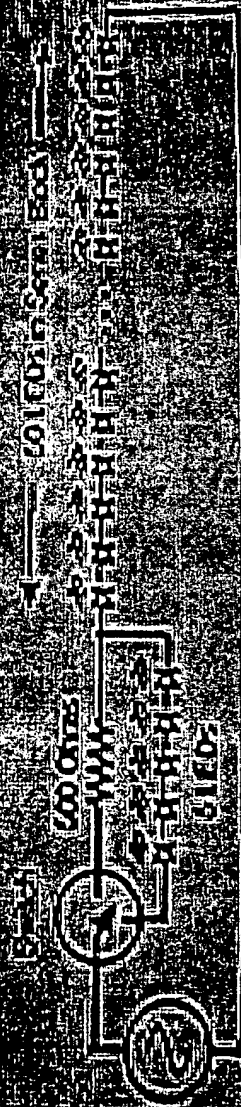
NOTICE

DR. MARK ALLEN, Ph.D., Chief Technical Officer, Fiber Optics Designs, Inc.

One can see that this circuit works just fine. There's current that runs through... here's the five extra LEDs for the other circuit...here on this branch is the resistor that's being used. This is a 500 ohm resistor. It's actually very hot right now. That's one of the reasons why one would want to omit this resistor is that it generates heat.

CIRCUIT DESCRIPTION

(Resistor 100 Ohm)



EXAMINER 070000

2011605355-1100

The second case is the examiner's option where five LEDs are being substituted for the resistor, so let's count them: one, two, three, four, five are being substituted for this resistor. By means of a switch we toggle...and the circuit fails immediately.

EXPERIMENT #1 RESULTS

Yamuro circuit (resistor included)

is stable.

Examiner option circuit

(no resistor) is unstable

IT FAILS

CONCLUSIONS

50 20 LEDs + 500 Ohm resistor

does not equal

55 20 LEDs



CONCLUSIONS

The resistor stabilizes the current by reducing the DC source voltage by 10VDC,

thereby linearizing the I-U characteristic

curve of the LEDs such that 2-volts DC falls within an acceptable LED operating region on the curve.

CONCLUSIONS

Cannot directly substitute LEDs for resistor.

Can not sum the LED DC specified voltage values to match an

AC source voltage.

CONCLUSIONS

Paramo circuit is not stable
(fails) when resistor is
removed.

EXPERIMENT #2

PURPOSE:

Test the stability of the Yamuro circuit (resistor included) and the Examiner's Option circuit

[substitute LEDs for the resistor

such that the sum of the LED DC specified voltage = AC source

voltage of 120VAC]

METHOD:

Duplicate the Yamuro circuit by building an equivalent circuit using U.S. AC source

voltage... Experimental Circuit is identical

to Yamuro's circuit since $55\text{ }200\text{C LEDs} +$

$500\text{ }0\text{hm resistor @ }120\text{ VAC (experiment)} =$

$45\text{ }200\text{C LEDs} + 500\text{ }0\text{hm resistor @ }100\text{ VAC}$

(Yamuro Example).

LEDs Used (Manufacturer's Specifications):

2.0 V_f DC at 20mA (typical)

2.4 V_f DC at 20mA (maximum)

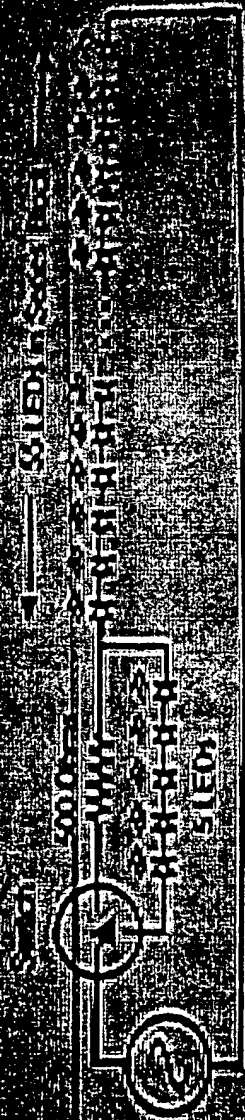
Resistor Used: 500 Ohms

Assumes 120 VAC

typical in the USA:

CIRCUIT DESCRIPTION

(Diagram 1 to 11)



WINDING CIRCUIT

2000Ω/5.455

1000

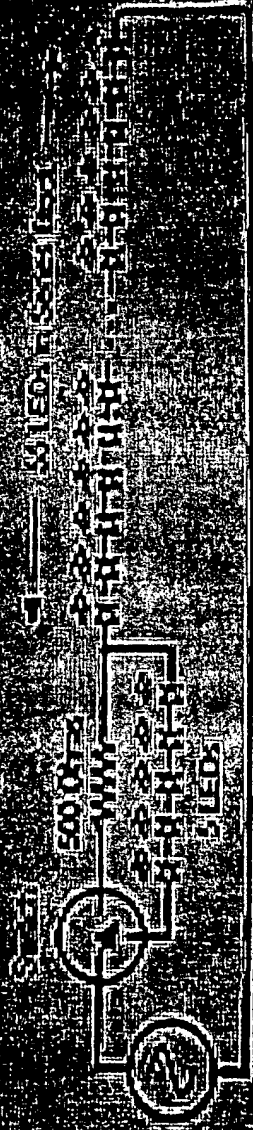
(1000 = 1000)

5000Ω/1000

So I have the toggle set...switch...set...to resistor...I plug it in. Notice that the circuit is working. Let's wait for awhile and notice further that the circuit works for some period of time.

CIRCUIT DESCRIPTION

Described in 001



EXHIBITS

2010000000

1211

When I do flick the switch to account for the examiner option, instead of the Yamuro circuit, we will be able to see that it will indeed fail. So, are you ready here camera man? I will flick the switch from the Yamuro circuit to the examiner's circuit. First we see that the circuit is being stressed. What's happening here is that the LEDs are over-voltaged again and they are starting to fail. We'll wait a few seconds. These things are hot! One just failed immediately. Here's another one that's failed...here's another one that's failed...the whole thing has finally failed. Once a few fail it's an avalanche process because there are less and less LEDs to take up the voltage in the circuit.

EXPERIMENT #2 RESULTS

Examiner circuit (resistor included)

is stable.

Examiner option circuit

(no resistor) is unstable

it fails

CONCLUSIONS

55 20 LEDs + 500 Ohm resistor

does not equal

60 20 LEDs

CONCLUSIONS

The resistor stabilizes the current by reducing the AC source voltage by 10 VAC,

thereby linearizing the I-V characteristic

curve of the LEDs such that 2-volts DC falls

within an acceptable LED operating region

on the curve.

CONCLUSIONS

Can not directly substitute LEDs for resistor.

Can not sum the LED DC specified voltage values to match an

AC source voltage.

CONCLUSIONS

Yamuro chem is insoluble
(tails) when resistor is

removed.

Yamuro chem is insoluble
(tails) when resistor is

EXPERIMENT #3

PURPOSE:

Test the stability of the Vamurong circuit (resistor included) and the examiner's Option circuit (substitute LEDs for the resistor such that the sum of the LED AC specified voltage = AC source voltage of 110VAC)

METHOD:

Duplicate the Yamuro circuit by building an equivalent circuit using U.S. AC source voltage... Experimental Circuit is identical

to Yamuro's circuit since $55\text{ VDC} / \text{LEDs} \rightarrow$

$500\text{ Ohm resistor @ } 110\text{--}120\text{VAC}$

(experiment) $\approx 45\text{ VDC} / \text{LEDs} \rightarrow 500\text{ Ohm}$

resistor @ 100VAC (Yamuro Example).

LEDs Used (Manufacturer's Specifications):

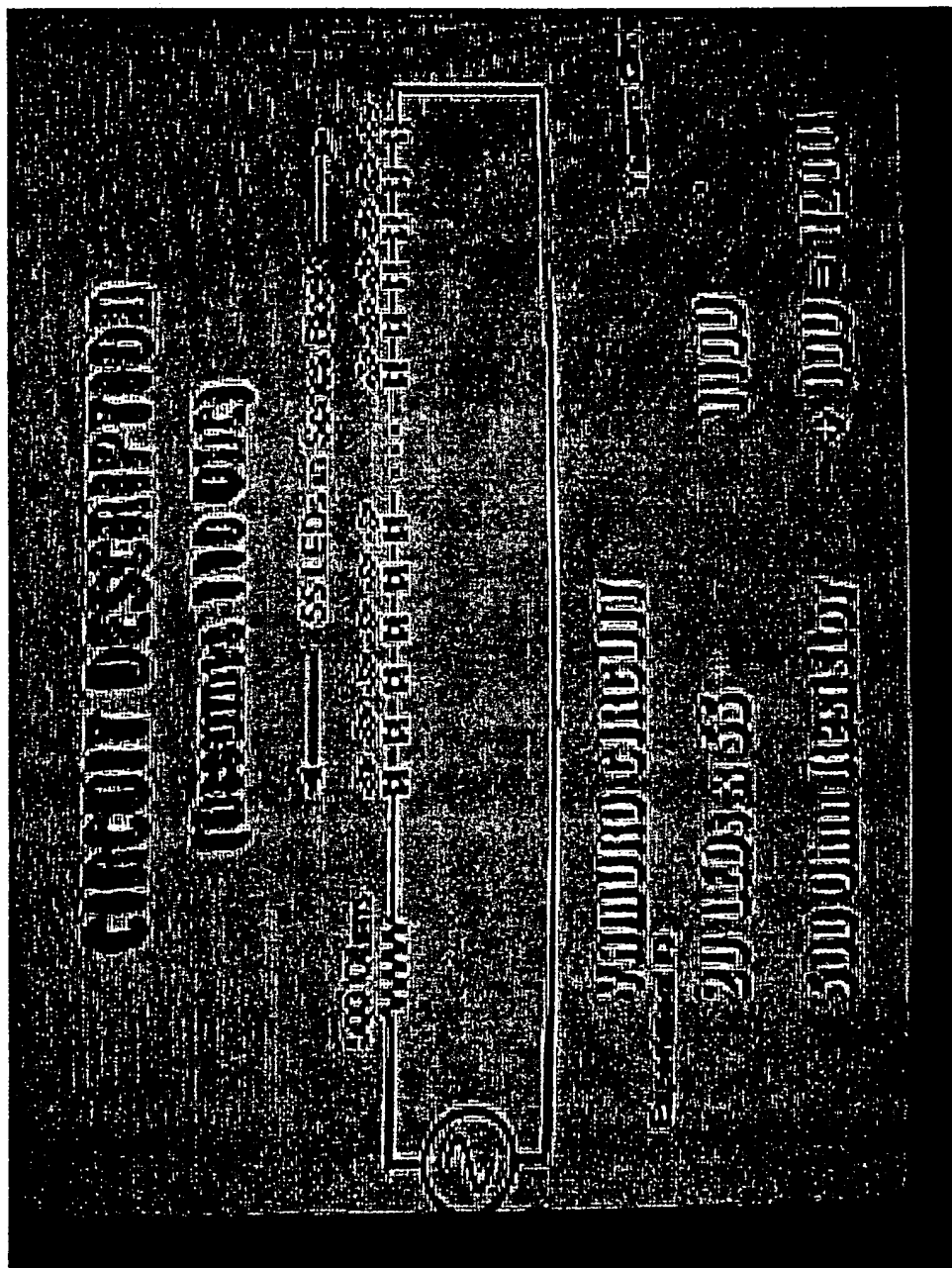
2.0 V_f DC at 20mA (typical)

2.4 V_f/DC at 20mA (maximum)

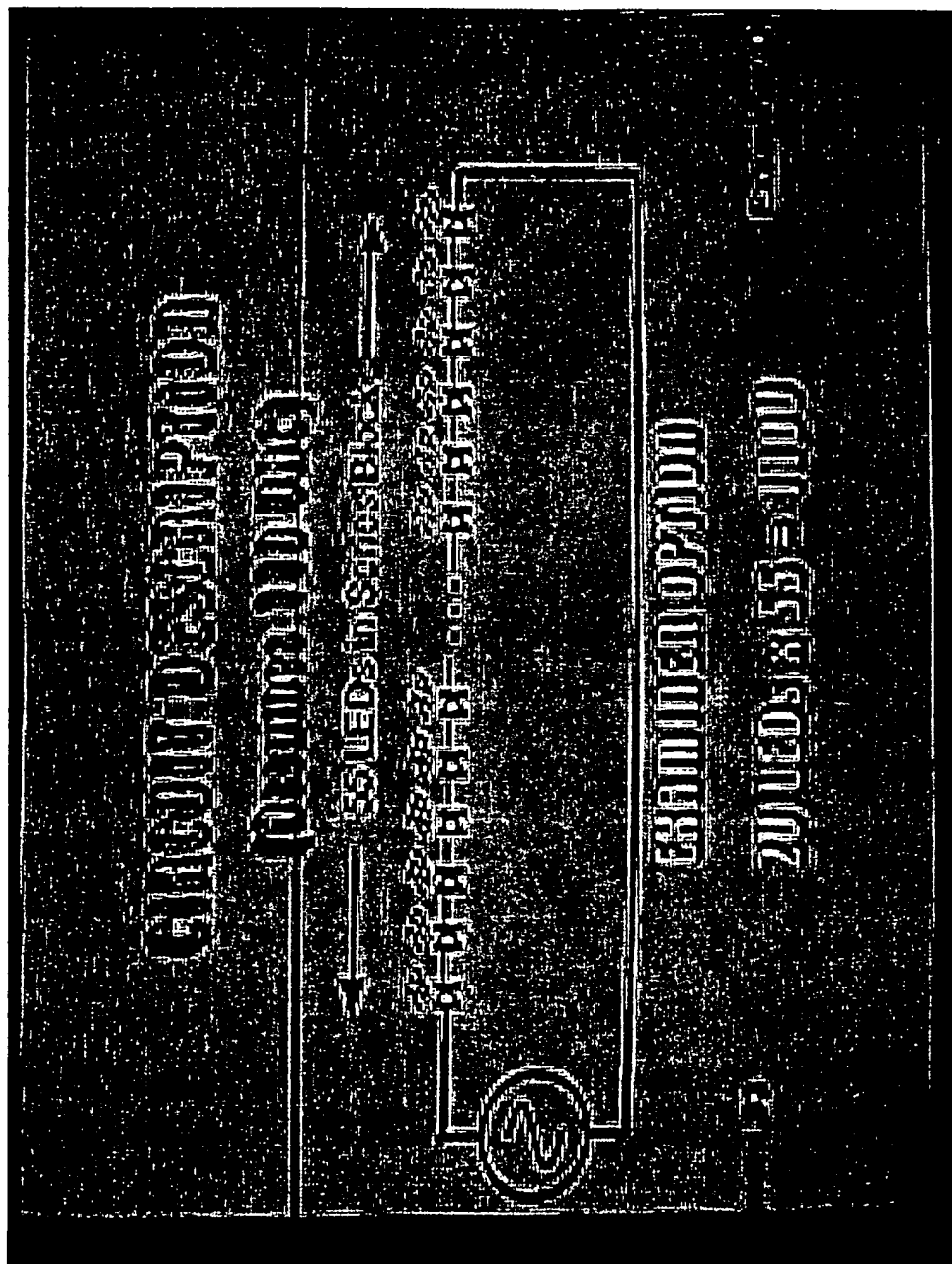
Note: Different color LEDs were selected

Resistor Used: 500 Ohms

Assumes 110 VAC typical in the USA



I have a resistor on here that's 500 Ohms as before. That's what we just demonstrated. I can plug this circuit in, and show that this circuit, which is the same as Yamuro's, works just fine. Notice that the brightness is approximately the same [as our circuit, edited out] and this resistor is getting very hot actually.



Now, as an alternative to this, one can take the resistor out and join these wires together. Now remember that there are 55 LEDs in this circuit. Have the attorney verify this. I'll join these two wires so this is the same as that 110 volt case for the examiner. The examiner's option where there is no resistor used. I'll plug it in and...it failed. Rather quickly in fact. I wouldn't want to sell this to somebody.

EXPERIMENT #3 RESULTS

Examined circuit (resistor included)

is stable.

Examiner option circuit

(no resistor) is unstable

IT FAILS

CONCLUSIONS

5520 LEDs + 500 Ohm resistor
@ 120 VAC

does not equal

5520 LEDs @ 110 VAC

CONCLUSIONS

The resistor stabilizes the circuit by reducing the DC source voltage/hydrovane,

thereby linearizing the I-U characteristic

curve of the LEDs so that 2-volts DC falls within an acceptable LED operating region on the curve.

CONCLUSIONS

Can not sum the LED DC specified voltage values to match an

AC source voltage.

LED color does not affect stability.

CONCLUSIONS

Pumpino circuit is undisturbed
(stable) when resistor is

removed.

PHOTOGRAPH OF PUMPINO CIRCUIT

EXPERIMENT #4

Note: Higher voltage LEDs used

PURPOSE:

Test the stability of the Yamuro circuit (resistor included) and the Examiner's

Option circuit (substitute LEDs for the

resistor such that the sum of the voltage specified) voltage = AC source voltage of

120VAC) using higher voltage LEDs.

METHOD:

Duplicate the Yamuro circuit by building an equivalent circuit using U.S. AC source voltage... Experimental Circuit is identical

to Yamuro's circuit since $55\ 2.2\text{VDC LEDs} +$

$500\ \text{Ohm resistor @ } 120\text{--}130\text{VAC}$

$(\text{experiment}) = 45\ 2\text{VDC LEDs} + 500\ \text{Ohm}$

$\text{resistor @ } 100\text{VAC (Yamuro example)}.$

LEDs Used (Manufacturer's Specifications):

2.2 V_f DC at 20mA (typical)

Resistor Used: 500 Ohms

Assumes 120 VAC typical in the USA

I've put a resistor in here according to Yamuro. The resistor value has 500 Ohms again just like all the resistors that are used to account for some ten-Volt difference. We're just simply going to show that that circuit, when I plug this in will be stable. Notice that there is no discoloration like we first saw in that 120 Volt example before it failed. It's the same color that one would expect. It's a stable circuit.

GETTING DESIGN DONE

BRIDGE

॥ श्रीगणेशाय नमः ॥

THE UNIVERSITY OF CHICAGO PRESS

THE HILLTOP

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Now, what we'll do is the same as before in the sense of removing this resistor and constructing the examiner's option for 110 Volts. Except now we're using LEDs whose specification is 2.2 Volts rather than 2 Volts. We have a 10% margin here in voltage. I will plug this in again. Are you ready? A little bit of apprehension on my part...and it failed immediately again.

EXPERIMENT #4 RESULTS

Examined circuit (resistor included)

is stable.

Examiner option circuit

(no resistor) is unstable

It fails

CONCLUSIONS

5521200F UEDS & 500 Ohm resistor

@ up to 131 VAC

does not equal

55222V UEDS @ 121 VAC

CONCLUSIONS

The resistor stabilizes the circuit by reducing the AC source voltage by 100VAC,

thereby linearizing the I-U characteristic

curve of the LEDs such that 2.2-volts DC falls within an acceptable LED operating

region on the curve.

CONCLUSIONS

Can not sum the LED DC specified voltage values to match an

AC source voltage.

Amuro circuit is not stable (fails)

when resistor is removed.

OVERALL CONCLUSIONS

LED Circuits are NOT stable
(fail) when designed

1. Using LED DC specified

voltage values to match any
AC source voltages, and...

2. Using LED AC specified

OVERALL CONCLUSIONS

LED Circuits are NOT stable
(fully) when designed

2. Without a resistor to linearize the I-U

characteristics curve of the circuit such

that the DC specified operating voltage of

the LEDs intersect the curve at a stable

operating region.

DAVID ALLEN:

Now, I would like to turn your attention once again to Dr. Mark Allen, Chief Technical Officer for Fiber Optic Designs, and Inventor. Dr. Allen possesses an earned Ph.D. in electrical engineering from the University of Pennsylvania and has worked as a high level research engineer for the past 15 years. Dr. Allen will explain the theories behind this invention.

DR. MARK ALLEN:

Let me explain the difference between AC and DC circuitry as applied to LEDs. LEDs are highly non-linear devices...that is, if one were to plot voltage versus current, one would end up with something different than a straight line. A resistor on the other hand...if I plot, say, current versus applied voltage here...is a straight line...it's a linear device. It follows Ohm's Law. Ohm's Law is $V = IR$...voltage equals current times resistance...so the resistance is the voltage divided by the current...so the slope here is related to the resistance. Actually, the slope of this straight line is simply one over the resistance, since I've plotted "I" versus "V."

An LED, on the other hand, is very different. If I were to plot "I" versus applied voltage...DC voltage...what would happen is that, for any voltage below some threshold voltage, the current is zero, including all negative voltages...the current is still zero. Above some threshold voltage, the current increases to some value such that the LED blows up...it breaks...so I'll call that V-max. This is Volts in voltages, uh, voltage in Volts, and this is current...I'll call it milliamps (mA). With LEDs, they're specified in DC...they're specified at a value where the current is considered to be stable at DC...where the device lasts a very long time. So it's the current that's significantly below this maximum current and it's the current where the device is going to last long. Typically, a value of about 20mA is specified for LEDs. For LEDs that we use throughout the demonstration, the specified current is 20 mA. For these LEDs, the voltage that produces 20 mA on the average is called *the* voltage. That's a DC voltage.

In the applicant's design, we don't consider DC voltages because our circuit is an AC circuit. What happens in an AC circuit is that, if I look at the waveform, instead of having some constant value...this is voltage going along this direction...instead of having a constant DC value

which produces a DC current here, I have an AC value which goes up, and comes down, and goes up, and comes down...like a sinusoid. What happens here is that, if I were to plot the current as a function of time...in the same way that I plotted voltage...for DC I had a straight line here...this is "I"...it would produce a DC current. For AC voltage, like Okuno pointed out, the voltage...if the voltage looks like this...the current above some threshold turns on and it goes up and produces a very high spike, and it turns back off, and during the entire negative part of the cycle, the current is zero. So, there's two things of interest here. If this is one cycle, there is an average value of current that's produced for AC. Moreover, there is a peak value of current that's produced at AC.

What's done in our design is to pick values for maximum current and some average value of current...I-avg...and find the voltages by measurement. So, we know that the AC voltage is going to be somewhere above zero. So one would construct...and I'll do this in red to show the difference between this and DC...one would measure the voltage and find that it's another curve that looks like this. Now, if we had DC values here for the demonstration that we use, we have 2.0 Volts producing 20mA for DC. When we do AC measurements, we find that a lower value of average RMS voltage...that is...if I take this sinusoid, I square it and then I take the average of that...which is the square root of 2 over 2 times the peak...V-rms. This is the voltage that I plotted here, V-rms. For example in US household current, it's 110 Volts RMS...the peak value is 2 over the square root of 2...or the square root of 2...times the RMS value. So this would be 110 and this would correspond to 155 Volts. So what happens is, if I take a variable voltage AC source and dial in different voltages, and take measurements of average current, I'm going to produce a curve with little measurement points that looks like this...and what I want to do is find

out what kind of voltage in here produces what average -Amp current that I want...what average AC voltage produces what AC average current that I want.

Well, the guidelines show that AC current is not necessarily a value of 20mA. Even the value of 20mA that is specified is for certain LED longevity. It could have been designed at 25mA or 15mA. But somewhere around 20mA is what's recommended. HP, for example, recommends that AC average current is somewhat larger than that...it should be somewhere around, oh, 40mA. What's also shown in specifications is the current that these things generally blow up...the DC current...this is somewhere around 100 mA.

What we have done in our design is make sure that this average current right here is about 40mA, and this peak current is well less than 100...typically, a value of 90 mA is used. So one looks at this curve and finds what value of peak is produced and what value of average is produced. Well, for 40 mA, the average value is about 1.6 Volts. That's why we end up with a series block of 70 LEDs versus 50 LEDs or 55 LEDs. It's because 110 Volts divided by 1.6 Volts AC...these are both AC quantities...is equal to 70. Actually it's 69, so when we round it, we get about 70 LEDs. Notice that this number is the *minimum* number of LEDs that one would use...it's not a *maximum* number of LEDs.

In other designs in the past, the idea was to put a resistor in there to try to help linearize this function...to add resistance to this...so this function may move over a little bit and become more and more linear....that's adding in a resistor or some kind of impedance device. Instead, what we've done is remove this impedance device and concentrate on this curve...this AC current versus AC voltage curve here...and design the apparatus such that an average voltage of 1.6 Volts is produced for this particular design, where the specified voltage is 2.0 Volts DC. This is AC.

This is DC. One can notice from this curve...and I have drawn it in such a way...or at least I've tried...that, if one follows this curve up at 2.0 Volts, we're into the region of instability. That's precisely what we have demonstrated, in fact. If we use DC values for AC, what happens is that the current involved...the average AC current...is much higher than one would expect...and the system is driven so high, in fact, that the LED breaks down, and the circuit doesn't work...it's unstable.

In the past, the only method of alleviating this was to add resistance or impedance and linearize this curve...move it over. What we do is leave the curve the same but use an AC value for the voltage, rather than previously specified values for the voltage, and simply measure according to a criterion. Here, we've defined 40mA as being the average current...we measure what voltage produces that...the 1.6 corresponds to 40mA. If we use another value, say 30mA, we'd have yet a lower value of voltage, or, with 20mA, a lower value of voltage still.

3mm ROUND TYPE Electrical / Optical Characteristics at T_A = 25°C

PART NUMBER	CHIP		LEN'S COLOR	I _r = 20mA					
	MATERIAL	EMITTING COLOR		λ _p (nm)	V _f (v)		I _v (mcd)		2θ _{1/2} (Deg.)
				TYP.	TYP.	MAX.	MIN.	TYP.	
LT0371(2)-41-M1	GaAlAs / GaAs	RED	R.D.(W.D.)	660	1.7	2.2	70	110	56
LT0371(2)-41	GaAlAs / GaAs	SB RED	R.D.(W.D.)	660	1.7	2.2	110	180	56
LT0371(2)-41-UR	GaAlAs / GaAlAs	UR RED	R.D.(W.D.)	660	1.9	2.5	210	350	56
LT0321(2)-41-HE	GaP / CaP	HE GREEN	G.D.(W.D.)	567	2.1	2.6	20	30	56
LT0331(2)-41-M1	InGaAlP/CaAs	YELLOW	Y.D.(W.D.)	593	2.0	2.4	24	40	56
LT0331(2)-41-UR	InGaAlP/CaAs	UR YELLOW	Y.D.(W.D.)	593	2.0	2.4	70	120	56
LT0373(4)-41-M1	GaAlAs / GaAs	RED	W.C.(R.C.)	660	1.7	2.2	210	360	36
LT0373(4)-41	GaAlAs / GaAs	SB RED	W.C.(R.C.)	660	1.7	2.2	360	600	36
LT0373(4)-41-UR	GaAlAs / GaAlAs	UR RED	W.C.(R.C.)	660	1.9	2.5	700	1200	36
LT0323(4)-41-HE	GaP / CaP	HE GREEN	W.C.(G.C.)	567	2.1	2.6	70	110	36
LT0333(4)-41-M1	InGaAlP/CaAs	YELLOW	W.C.(Y.C.)	593	2.0	2.4	72	120	36
LT0333(4)-41-UR	InGaAlP/CaAs	UR YELLOW	W.C.(Y.C.)	593	2.0	2.4	240	400	36

5mm ROUND TYPE Electrical / Optical Characteristics at T_A = 25°C

PART NUMBER	CHIP		LEN'S COLOR	I _F = 20mA					
	MATERIAL	EMITTING COLOR		λ _P (nm)	V _F (v)		I _V (mcd)		2θ 1/2 (Deg.)
				TYP.	TYP.	MAX.	MIN.	TYP.	
LT1871(2)-81-M1	GaAlAs / GaAs	RED	R.D.(W.D.)	660	1.7	2.2	108	180	36
LT1871(2)-81	GaAlAs / GaAs	SB RED	R.D.(W.D.)	660	1.7	2.2	180	300	36
LT1871(2)-81-UR	GaAlAs / GaAlAs	UR RED	R.D.(W.D.)	660	1.9	2.5	360	600	36
LT1821(2)-81-HE	GaP / GaP	HE GREEN	G.D.(W.D.)	567	2.1	2.6	60	100	36
LT1831(2)-81-M1	InGaAlP/GaAs	YELLOW	Y.D.(W.D.)	593	2.0	2.4	144	240	36
LT1831(2)-81-UR	InGaAlP/GaAs	UR YELLOW	Y.D.(W.D.)	593	2.0	2.4	240	400	36
LT18J1(2)-81-M1	InGaAlP/GaAs	ORANGE	O.D.(W.D.)	621	2.0	2.4	144	240	36
LT1873(4)-81-M1	GaAlAs / GaAs	RED	W.C.(R.C.)	660	1.7	2.2	360	600	22
LT1873(4)-81	GaAlAs / GaAs	SB RED	W.C.(R.C.)	660	1.7	2.2	600	1000	22
LT1873(4)-81-UR	GaAlAs / GaAlAs	UR RED	W.C.(R.C.)	660	1.9	2.5	1200	2000	22
LT1823(4)-81-HE	GaP / GaP	HE GREEN	W.C.(G.C.)	567	2.1	2.6	300	500	22
LT1833(4)-81-M1	InGaAlP/GaAs	YELLOW	W.C.(Y.C.)	593	2.0	2.4	250	420	22
LT1833(4)-81-UR	InGaAlP/GaAs	UR YELLOW	W.C.(Y.C.)	593	2.0	2.4	840	1400	22
LT18J3(4)-81-M1	InGaAlP/GaAs	ORANGE	W.C.(O.C.)	621	2.0	2.4	250	420	22
LT2K73(4)-81-M1	GaAlAs / GaAs	RED	W.C.(R.C.)	660	1.7	2.2	400	700	20
LT2K73(4)-81	GaAlAs / GaAs	SB RED	W.C.(R.C.)	660	1.7	2.2	700	1200	20
LT2K73(4)-81-UR	GaAlAs / GaAlAs	UR RED	W.C.(R.C.)	660	1.9	2.5	1400	2400	20
LT2K23(4)-81-HE	GaP / GaP	HE GREEN	W.C.(G.C.)	567	2.1	2.6	350	583	20
LT2K33(4)-81-M1	InGaAlP/GaAs	YELLOW	W.C.(Y.C.)	593	2.0	2.4	280	467	20
LT2K33(4)-81-UR	InGaAlP/GaAs	UR YELLOW	W.C.(Y.C.)	593	2.0	2.4	1000	1600	20



Series

5 mm Round LEDs, T-1 3/4

Part Number EL-xxxxx	Chip			Lens Color	Vf(V) at If=20mA		Iv(mcd)			Angle 2θ 1/2
	Material	Emitted Color	λp (nm)		Typ.	Max.	at	at If=20 mA		
							If=2mA Typ.	Min.	Typ.	
EL-333-2UBC	GaN/SiC	Blue	430	Water Clear	3.8	4.5	---	250	400	10
EL-333-2YGC/S530-A2	AlGaInP	Super Green	574	Water Clear	2.0	2.4	14	126	239	10
EL-333-2YGC/S530-A3	AlGaInP	Super Green	574	Water Clear	2.0	2.4	27	239	453	10
EL-333-2YGC/S530-A4	AlGaInP	Super Green	574	Water Clear	2.0	2.4	372	453	611	10
EL-333-2YGC/S530-A5	AlGaInP	Super Green	574	Water Clear	2.0	2.4	43	611	724	10
EL-333-2YGC/S400-A3	AlGaInP	Super Green	574	Water Clear	2.0	2.4	30	380	506	10
EL-333-2YGC/S400-A4	AlGaInP	Super Green	574	Water Clear	2.0	2.4	41	506	683	10
EL-333-2YGC/S400-A5	AlGaInP	Super Green	574	Water Clear	2.0	2.4	49	683	810	10
EL-333-2UYC/S530-A2	AlGaInP	Super Yellow	591	Water Clear	2.0	2.4	61	405	1013	10
EL-333-2UYC/S530-A3	AlGaInP	Super Yellow	591	Water Clear	2.0	2.4	89	945	1485	10
EL-333-2UYC/S530-A4	AlGaInP	Super Yellow	591	Water Clear	2.0	2.4	122	1350	2025	10
EL-333-2UYC/S530-A5	AlGaInP	Super Yellow	591	Water Clear	2.0	2.4	154	1620	2565	10
EL-333-2UYC/S530-A6	AlGaInP	Super Yellow	591	Water Clear	2.0	2.4	178	2025	2970	10
EL-333-2UYC/S400-A3	AlGaInP	Super Yellow	591	Water Clear	2.0	2.4	95	1003	1575	10
EL-333-2UYC/S400-A4	AlGaInP	Super Yellow	591	Water Clear	2.0	2.4	129	1432	2148	10
EL-333-2UYC/S400-A5	AlGaInP	Super Yellow	591	Water Clear	2.0	2.4	163	1719	2721	10
EL-333-2UYC/S400-A6	AlGaInP	Super Yellow	591	Water Clear	2.0	2.4	189	2148	3151	10
EL-333-2USOC/S530-A2	AlGaInP	Super Sunset Orange	621	Water Clear	2.0	2.4	65	431	1077	10
EL-333-2USOC/S530-A3	AlGaInP	Super Sunset Orange	621	Water Clear	2.0	2.4	95	1005	1579	10
EL-333-2USOC/S530-A4	AlGaInP	Super Sunset Orange	621	Water Clear	2.0	2.4	129	1436	2154	10
EL-333-2USOC/S530-A5	AlGaInP	Super Sunset Orange	621	Water Clear	2.0	2.4	164	1723	2728	10
EL-333-2USOC/S530-A6	AlGaInP	Super Sunset Orange	621	Water Clear	2.0	2.4	190	2154	3159	10
EL-333-2USOC/S400-A3	AlGaInP	Super Sunset Orange	621	Water Clear	2.0	2.4	108	1143	1795	10
EL-333-2USOC/S400-A4	AlGaInP	Super Sunset Orange	621	Water Clear	2.0	2.4	147	1632	2448	10
EL-333-2USOC/S400-A5	AlGaInP	Super Sunset Orange	621	Water Clear	2.0	2.4	186	1959	3101	10
EL-333-2USOC/S400-A6	AlGaInP	Super Sunset Orange	621	Water Clear	2.0	2.4	216	2448	3592	10
EL-333-2SURC/S530-A2	AlGaInP		632	Water Clear	2.0	2.4	50	344	837	10
EL-333-2SURC/S530-A3	AlGaInP		632	Water Clear	2.0	2.4	72	803	1204	10
EL-333-2SURC/S530-A4	AlGaInP		632	Water Clear	2.0	2.4	98	1101	1639	10
EL-333-2SURC/S530-A5	AlGaInP		632	Water Clear	2.0	2.4	122	1330	2041	10
EL-333-2SURC/S530-A6	AlGaInP		632	Water Clear	2.0	2.4	148	1605	2465	10
EL-333-2SURC/S400-A3	AlGaInP		632	Water Clear	2.0	2.4	81	905	1358	10
EL-333-2SURC/S400-A4	AlGaInP		632	Water Clear	2.0	2.4	111	1241	1849	10
EL-333-2SURC/S400-A5	AlGaInP		632	Water Clear	2.0	2.4	138	1500	2301	10
EL-333-2SURC/S400-A6	AlGaInP		632	Water Clear	2.0	2.4	167	1810	2780	10
EL-333-2USRC/S530-A2	AlGaInP		639	Water Clear	2.0	2.4	38	298	628	10
EL-333-2USRC/S530-A3	AlGaInP		639	Water Clear	2.0	2.4	51	532	851	10
EL-333-2USRC/S530-A4	AlGaInP		639	Water Clear	2.0	2.4	64	639	1064	10
EL-333-2USRC/S530-A5	AlGaInP		639	Water Clear	2.0	2.4	83	851	1384	10
EL-333-2USRC/S530-A6	AlGaInP		639	Water Clear	2.0	2.4	102	1064	1703	10
EL-333-2USRC/S400-A3	AlGaInP		639	Water Clear	2.0	2.4	57	596	954	10
EL-333-2USRC/S400-A4	AlGaInP		639	Water Clear	2.0	2.4	72	716	1193	10
EL-333-2USRC/S400-A5	AlGaInP		639	Water Clear	2.0	2.4	93	954	1551	10
EL-333-2USRC/S400-A6	AlGaInP		639	Water Clear	2.0	2.4	115	1193	1909	10
EL-333UWC/1B	GaN	Super Blue	----	Water Clear	3.6	4.0	----	500	1250	10
EL-333UWC	GaN	Super Blue	----	Water Clear	3.6	4.0	----	2200	3300	10

Merlight 575nm

RED

HE GREEN

15
2
96
7.63.5
2
70

113.6

黄色

GaAsP/GaP YELLOW LED CHIPS

DEVICE NO. : ED-011HYH

1. SCOPE :

THIS SPECIFICATION APPLIES TO GaAsP/GaP YELLOW LED CHIPS,
DEVICE NO. ED-011HYH.

2. STRUCTURE :

2-1. MESA TYPE: SMOOTH SURFACE.

2-2. ELECTRODES :

P (ANODE) SIDE : ALUMINIUM ALLOY OR GOLD ALLOY.

N (CATHODE) SIDE : GOLD ALLOY.

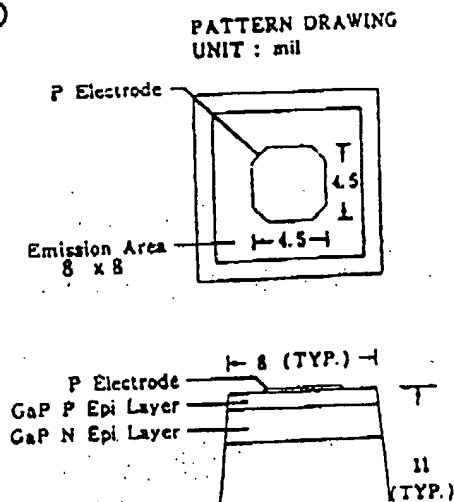
3. SIZE :

3-1. CHIP SIZE : 10 mils \times 10 mils (0.254 mm \times 0.254 mm).

3-2. PATTERN DRAWING : REFER TO THE ATTACHED DRAWING.

4. ELECTRO-OPTICAL CHARACTERISTICS ($T_a = 25^\circ\text{C}$)

PARAMETER	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
FORWARD VOLTAGE	V_f	$I_f=20\text{mA}$		2.20	2.60	V
REVERSE CURRENT	I_r	$V_r=5\text{V}$			10	μA
LUMINOUS INTENSITY	I_v	$I_f=20\text{mA}$	3.0			mcd
SPECTRUM WIDTH OF HALF VALUE	$\Delta\lambda$	$I_f=20\text{mA}$		35		nm
WAVELENGTH	λ_p	$I_f=20\text{mA}$		589		nm
	λ_d			590		



Operational Considerations for LED Lamps and Display Devices

Application Note 1005

In the design of a drive circuit for an LED lamp, an LED light bar, or an LED 7-segment display, the objective is to achieve optimum light output, power dissipation, reliability, and operating life. The performance capabilities of each LED device are presented in the device data sheet. The data sheet contains tabular data and graphs that describe the optical and electrical characteristics of the LED device, and Absolute Maximum Ratings which are the maximum operating capabilities of the device. A thorough understanding of how to use this information is the basis for achieving an optimum design.

This application note presents an in-depth discussion of the use of the optical and electrical information contained in an LED device data sheet. Design examples for dc and pulsed operation are presented. The calculated results for each example are in **Bold Type** for identification.

Typical Data Sheet Information

Data sheets typically contain three tables of data. Usually for LED lamp devices the first table is titled **Device Selection Guide** or

Axial Luminous Intensity and Viewing Angle at $T_A = 25^\circ\text{C}$ and presents the basic optical characteristics of the devices listed in the data sheet. The luminous intensity, I_v , both minimum and typical values, are listed in this table. This table is used as a device selection guide.

The next table is titled **Absolute Maximum Ratings at $T_A = 25^\circ\text{C}$** , containing maximum peak, dc and average currents, maximum transient current, operating and storage temperature range, and the absolute maximum LED junction temperature. These are the maximum allowed operating conditions for all the devices in the data sheet.

The third table, titled **Electrical/Optical Characteristics at $T_A = 25^\circ\text{C}$** , contains the electrical data, and some optical data, that are used to determine the operating conditions for the device. The forward voltage, V_F , and device thermal resistance, $R_{\theta J-PIN}$, used in operating condition calculations, are listed in this table.

The graphs usually contained in a lamp data sheet used to determine operational

conditions are:

Figure 1. Relative Intensity vs. Wavelength.
(not shown here)

Figure 2. Forward Current vs. Forward Voltage.

Figure 3. Relative Luminous Intensity vs. DC Forward Current.

Figure 4. Relative Efficiency vs. Peak Current.
(This figure is not included on all data sheets.)

Figure 5. Maximum Forward DC Current vs. Ambient Temperature.

Figure 6. Maximum Average Current vs. Peak Forward Current.

Figure 7. Relative Luminous Intensity vs. Angular Displacement.
(not shown here)

Design Criteria

The two criteria that establish the operating limits are the maximum

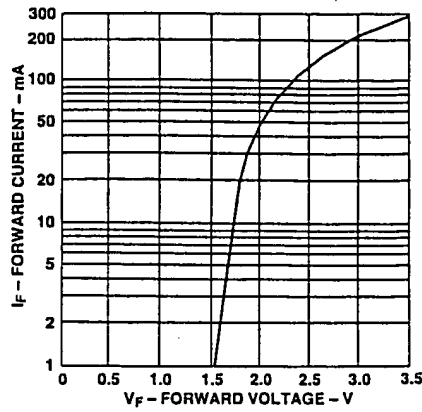


Figure 2. Forward Current vs. Forward Voltage.

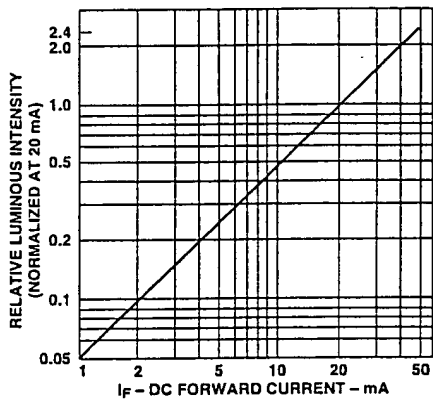


Figure 3. Relative Luminous Intensity vs. DC Forward Current.

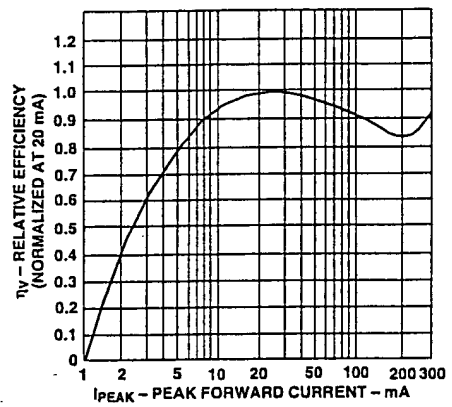


Figure 4. Relative Efficiency vs. Peak Forward Current.

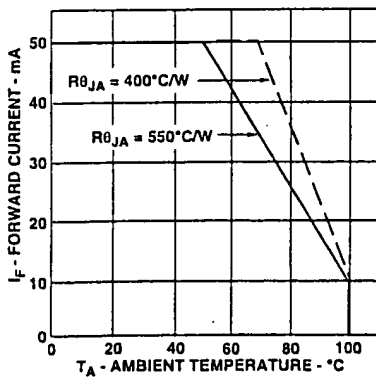


Figure 5. Maximum Forward DC Current vs. Ambient Temperature. Derating Based on $T_{JMAX} = 110^{\circ}\text{C}$.

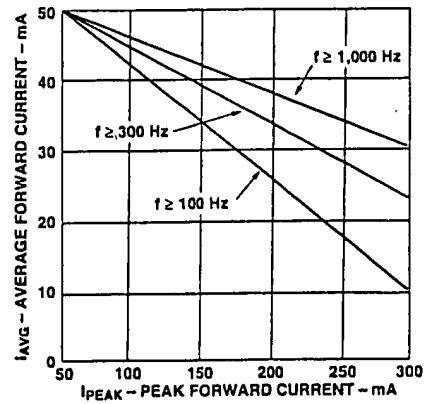


Figure 6. Maximum Average Current vs. Peak forward Current.

drive currents and the absolute maximum LED junction temperature, T_{JMAX} . The maximum drive currents have been established to ensure long operating life. The absolute maximum LED junction temperature is a device package limitation that must not be exceeded.

Thermal Resistance

The LED junction temperature, $T_J(^{\circ}C)$, is the sum of the ambient temperature, $T_A(^{\circ}C)$, and the temperature rise of the LED junction above ambient, $\Delta T_J(^{\circ}C)$, which is the product of the power dissipated within the LED junction, $P_D(W)$, and the thermal resistance LED junction-to-ambient, $R_{\theta J-A}(^{\circ}C/W)$.

$$\begin{aligned} T_J &= T_A + \Delta T_J \\ T_J &= T_A + P_D \times R_{\theta J-A} \end{aligned} \quad (1)$$

The cathode leads (pins) of a typical LED device are the primary thermal paths for heat dissipation from the LED junction to the surrounding environment. The exceptions are TS AlGaAs lamps, that use flip chip technology (anode die attach), where the anode lead is the primary thermal path. The data sheet lists the thermal resistance LED junction-to-pin, $R_{\theta J-PIN}(^{\circ}C/W)$, for each device type listed. This device thermal resistance is added to the pc board mounting assembly thermal resistance-to-ambient, $R_{\theta PC-A}(^{\circ}C/W)$, to obtain the overall thermal resistance LED junction-to-ambient, $R_{\theta J-A}(^{\circ}C/W)$.

$$R_{\theta J-A} = R_{\theta J-PIN} + R_{\theta PC-A} \quad (2)$$

$R_{\theta J-A}$ is on a per LED chip basis for lamps, light bars, and 7-segment displays, and on a per device basis for displays with on-board ICs.

For reliable operation, it is recommended that the value of $R_{\theta PC-A}$ be designed low enough to achieve the lowest possible $R_{\theta J-A}$ to ensure the LED junction temperature does not exceed the absolute maximum value when the device is operated in the maximum surrounding ambient temperature.

Maximum Power Calculation

The maximum allowed power that may be dissipated within an LED junction, P_{MAX} , is determined by multiplying the maximum rated dc current by the forward voltage for that current, determined from Figure 2.

$$P_{MAX} = I_{DCMAX} \times V_F \quad (3)$$

Derating vs. Temperature

The drive current derating vs. temperature, Figure 5, is a function of drive current, T_{JMAX} , and $R_{\theta J-A}$. Typically derating curves are given from two ambient temperatures, $T_A = 50^{\circ}C$ (solid line) and $70^{\circ}C$ (dashed line). The derating curves are lines of T_{JMAX} with slopes equal to the specific maximum $R_{\theta J-A}$ values indicated, intersecting the temperature axis at the maximum LED junction temperature point with zero current. Operation of the LED device at a particular drive current should be at or below a derating curve with a thermal resistance-to-ambient at or less than the maximum value indicated for that curve.

Current Limiting

An LED is a current operated device, and therefore, requires some kind of current limiting incorporated into the drive circuit. This current limiting typically takes the form of a current limiter resistor, R , placed in series with the LED.

The forward voltage characteristic of Figure 2 is used to calculate the value of the series current limiter resistor.

$$R = \frac{V_{CC} - V_{SAT} - V_F}{I_{PEAK}} \quad (4)$$

Where:

V_{CC} = Power supply voltage.

V_{SAT} = Saturation voltage of driver transistor(s).

V_F = Forward voltage of the LED at I_{PEAK} .

I_{PEAK} = The peak drive current through the LED.

Light Output

The luminous intensity at $T_A = 25^{\circ}C$ for a particular dc drive condition is determined using the relative luminous intensity factor from Figure 3.

$$I_V(dc) = [I_V(25^{\circ}C)] [\text{Relative Intensity Factor}] \quad (5)$$

Where: $I_V(25^{\circ}C)$ is obtained from the data sheet.

For pulsed drive conditions, the time average luminous intensity is determined from the relative efficiency characteristic, η_V , presented in Figure 4. (Note: Not all data sheets include relative efficiency data.)

$$I_V(\text{time average}) = [I_V(25^{\circ}C)] [I_{AVG}/I_F] [\eta_V] \quad (6)$$

Where:

$I_V(25^{\circ}C)$ = Data sheet luminous intensity value.

- I_{AVG} = The time average operating current.
 I_F = The current where the data sheet luminous intensity is specified.
 η_v = Relative efficiency factor for the peak drive current, I_{PEAK} .

The calculated luminous intensity value at $T_A = 25^\circ\text{C}$ can be adjusted for a different operating ambient temperature by the following exponential equation, and using the k factor for the specific LED.

$$I_V(T_A) = I_V(25^\circ\text{C})e^{k(T_A - 25^\circ\text{C})} \quad (7)$$

LED	k
Standard Red	-0.0188/ $^\circ\text{C}$
High Efficiency Red	-0.0131/ $^\circ\text{C}$
Yellow	-0.0112/ $^\circ\text{C}$
Green	-0.0104/ $^\circ\text{C}$
DH AS AlGaAs	-0.0095/ $^\circ\text{C}$
TS AlGaAs	-0.0130/ $^\circ\text{C}$
AlInGaP	-0.0100/ $^\circ\text{C}$
TS AlInGaP	-0.0100/ $^\circ\text{C}$

Pulsed Operation vs. DC Operation

When operating an LED device under dc drive conditions, the LED junction temperature is a linear function of the dc power dissipation multiplied by $R_{\theta J-A}$. The light output is proportional to the dc drive current by the luminous intensity factor of Figure 3 and as expressed in Equation 5.

For best pulsed operation and overall light output performance, a rectangular current waveform

with a refresh rate equal to or greater than 100 Hz is strongly recommended. Sinusoidal waveforms are not generally recommended, as the rms power will exceed that of a rectangular current waveform with the same peak current value. If a sinusoidal current waveform is used, the peak current should not exceed the maximum dc current rating. Sinusoidal waveforms produce less than two thirds the light output of an equivalent rectangular pulse, and at 50 or 60 Hz, are not fast enough to prevent observable flicker.

When operating an LED device in pulsed current mode, it is the peak junction temperature, not the average junction temperature, that governs the performance of the device. At refresh rates below 1000 Hz (the number of times per second a device is pulsed), the peak junction temperature is higher than the average junction temperature. As a result, the allowed time average currents for refresh rates between 100 Hz and 1000 Hz are less than those permitted at 1000 Hz, as can be seen by the 100 Hz and 300 Hz curves of Figure 6.

Design Steps

In order to determine the derated drive conditions from the data sheet for an elevated ambient temperature, the value for $R_{\theta J-A}$ must be determined. Once the value for $R_{\theta J-A}$ has been established, the required current derating can be determined for safe operation at the elevated temperature directly from Figure 5. The basic design steps are:

1. Determine $R_{\theta J-A}$.
2. Calculate the required value for $R_{\theta PC-A}$ for the pc board

mounting configuration.

3. Determine the maximum allowable dc drive current for the operating ambient temperature.
4. Calculate the LED chip power dissipation to be sure it will not cause T_J to exceed the absolute maximum value.
5. Calculate the value of the current limiting resistor.
6. Determine the luminous intensity at 25°C and at the elevated ambient temperature.

The example calculations in this application note use representative data typically contained in LED lamp data sheets. The purpose of the calculations is to ensure reliable operation of an LED lamp when operated at an elevated ambient temperature. For the example calculations, a sample T-1 3/4 LED lamp is used, with 0.45 mm (0.018 in.) square leads and the following data sheet parameters:

Typical Luminous Intensity at 20 mA, $I_V(25^\circ\text{C}) = 2.0$ cd (candela).

Maximum Peak Forward Current = 300 mA.

Maximum Average Forward Current = 30 mA ($I_{PEAK} = 300$ mA).

Maximum dc Forward Current = 50 mA.

Maximum LED Junction Temperature = 110°C .

$R_{\theta J-PIN} = 260^\circ\text{C/W}$

DC Design Example

In this example, the operating ambient temperature is assumed to be $T_A = 60^\circ\text{C}$.

Step 1. For this example, the value for $R_{\theta J-A}$ has been established to be 500°C/W .

Step 2.

From Equation 2:

$$R_{\theta PC-A} = (500 - 260^{\circ}\text{C/W})$$

$$R_{\theta PC-A} = 240^{\circ}\text{C/W}$$

The pc board mounting assembly should be designed to provide this value of thermal resistance to ambient, or less, for reliable operation of the LED device.

Step 3.

From Figure 5, the following are determined:

- 1) $R_{\theta PC-A}$ at 500°C/W is less than the maximum $R_{\theta PC-A}$ shown for the solid line derating curve.
- 2) The maximum allowable dc current at T_A of $60^{\circ}\text{C} = 42 \text{ mA}$.

Step 4.

Calculation of the power dissipation for 42 mA drive current using Equation 3.

From Figure 2, V_F (42 mA) = 1.95 volts.

$$P(W) =$$

$$(0.042 \text{ A}) (1.95 \text{ V}) = 0.082 \text{ W}$$

$$P(W) = 82 \text{ mW}$$

Using Equation 1 for LED junction temperature:

$$T_J = 60^{\circ}\text{C} + (0.082 \text{ W}) (500^{\circ}\text{C/W})$$

$T_J = 101^{\circ}\text{C}$, less than the maximum allowable 110°C .

Step 5.

Equation 4 is used to calculate the value of the current limiting resistor. A 5 volt power supply is used. One switching transistor is used to drive the LED lamp with a saturation of 0.1 volts.

$$R = \frac{5.0 \text{ V} - 0.1 \text{ V} - 1.95 \text{ V}}{0.042 \text{ A}}$$

$$R = 70 \Omega$$

Resistor power rating should be 2x the actual power dissipation:

$$P_R = I^2 \times R = (0.042 \text{ A})^2 \times 70 \Omega$$

$$P_R = 0.123 \text{ W}$$

Thus, use a 1/4 watt 70 Ω resistor.

Step 6.

The luminous intensity at $T_A = 25^{\circ}\text{C}$ is determined from Figure 3 and Equation 5:

From Figure 3, the relative luminous intensity factor at 42 mA = 2.0.

$$I_V (25^{\circ}\text{C}) = (2.0 \text{ cd}) (2.0)$$

$$I_V (25^{\circ}\text{C}) = 4.0 \text{ cd}$$

At the operating temperature of 60°C , the luminous intensity is calculated using Equation 7 and the appropriate k value. For this example, $k = -0.0130/^{\circ}\text{C}$

$$I_V (60^{\circ}\text{C}) =$$

$$(4.0 \text{ cd}) e^{-0.0130/^{\circ}\text{C}(60 - 25^{\circ}\text{C})}$$

$$I_V (60^{\circ}\text{C}) = (4.0 \text{ cd}) (0.634)$$

$$I_V (60^{\circ}\text{C}) = 2.54 \text{ cd}$$

DC parameter summary:

$$T_A = 60^{\circ}\text{C}$$

$$R_{\theta PC-A} = 240^{\circ}\text{C/W}$$

$$I_F (\text{dc}) = 42 \text{ mA}$$

$$T_J = 101^{\circ}\text{C}$$

$$R = 70 \Omega, 1/4 \text{ W}$$

$$I_V (25^{\circ}\text{C}) = 4.0 \text{ cd}$$

$$I_V (60^{\circ}\text{C}) = 2.54 \text{ cd}$$

Pulsed Mode Design Example

In this example, $T_A = 50^{\circ}\text{C}$, and the above LED lamp is to be pulsed with a refresh rate of 1000 Hz at 200 mA peak drive current.

Steps 1 and 2. The $R_{\theta J-A}$ and $R_{\theta PC-A}$ values are the same as determined in the above DC Design Example.

Step 3.

From Figure 6, at a refresh rate of 1000 Hz and I_{PEAK} of 200 mA, the maximum allowable time average current, $I_{AVG} = 38 \text{ mA}$.

The on-time duty factor, DF is:

$$DF = I_{AVG} / I_{PEAK}$$

$$DF = 38 \text{ mA} / 200 \text{ mA} = 0.190$$

$$DF = 19.0\%$$

Step 4.

From Figure 2, V_F (200 mA) = 2.8 volts. The time average power is:

$$P = I_{PEAK} \times V_F (I_{PEAK}) \times DF$$

$$P = (0.200 \text{ A}) (2.8 \text{ V}) (0.190)$$

$$P = 0.106 \text{ W}$$

Using Equation 1 for LED junction temperature:

$$T_J =$$

$$50^{\circ}\text{C} + (0.106 \text{ W}) (500^{\circ}\text{C/W})$$

$T_J = 103^{\circ}\text{C}$, less than the maximum allowable 110°C .

Step 5.

At 200 mA, the driver transistor saturation is 0.2 volts.

$$R = \frac{5.0 \text{ V} - 0.2 \text{ V} - 2.8 \text{ V}}{0.200 \text{ A}}$$

$$R = 10 \Omega$$

Resistor power rating should be 2x the time average power dissipation:

$$P_R = (I_{PEAK})^2 \times R \times DF$$

$$= (0.200 \text{ A})^2 (10 \Omega) (0.190)$$

$$P_R = 0.076 \text{ W}$$

Thus, use a 1/4 watt 10 Ω resistor.

Step 6.

The time average luminous intensity at $T_A = 25^{\circ}\text{C}$ is determined using Equation 6 and the relative efficiency factor from Figure 4.

From Figure 4,

$$\eta_v (200 \text{ mA}) = 0.82.$$

$$I_v (25^\circ\text{C}) = [2.0 \text{ cd}] [38 \text{ mA}/20 \text{ mA}] [0.82]$$

$$I_v (25^\circ\text{C}) = [2.0 \text{ cd}] [1.56]$$

$$I_v (25^\circ\text{C}) = 3.12 \text{ cd}$$

$$I_v (50^\circ\text{C}) = (3.12 \text{ cd}) e^{-0.0130/^\circ\text{C}(50 - 25^\circ\text{C})}$$

$$I_v (50^\circ\text{C}) = (3.1 \text{ cd}) (0.723)$$

$$I_v (50^\circ\text{C}) = 2.26 \text{ cd}$$

Pulsed parameter summary:

$$T_A = 50^\circ\text{C}$$

$$R_{\theta\text{PC-A}} = 240^\circ\text{C/W}$$

$$I_{\text{PEAK}} = 200 \text{ mA}$$

$$I_{\text{AVG}} = 38 \text{ mA}$$

$$f = 1000 \text{ Hz}; \text{DF} = 19.0\%$$

$$T_J = 103^\circ\text{C}$$

$$R = 10 \Omega, 1/4 \text{ W}$$

$$I_v (25^\circ\text{C}) = 3.12 \text{ cd}$$

$$I_v (60^\circ\text{C}) = 2.26 \text{ cd}$$

DC Operation is Better than Pulsed Operation for Light Output

It is always better to drive an LED device with a high dc current to obtain the necessary light output to be viewed by a human observer than to pulse drive the LED. Using a high peak current and a low duty factor to pulse drive an LED device produces less time average light output than by using a high dc drive current.

There are only two reasons for pulse driving an LED device:

- 1) To strobe an LED array to form messages of changing characters or symbols to be viewed by human observers.
- 2) To obtain a peak pulse of light to be received by a photodetector in a non-visual emitter/detector application. In this case, the high peak pulse of light produces a high peak photocurrent output from the photodetector.

Operation Without Current Derating

LED lamp and display devices may be operated in elevated ambient temperatures without current derating only when the pc board mounting configuration is designed for a sufficiently low thermal resistance-to-ambient. The criterion is that the LED junction temperature must not exceed the $T_{J\text{MAX}}$ value for the device. This low thermal resistance design may include such items as a maximum metalized pc board and possible heat sinking to ensure adequate heat dissipation. Operation above the Absolute Maximum Current Ratings is not recommended.

The necessary thermal resistance requirements for operation without current derating are calculated for the maximum power dissipation using the Absolute Maximum DC Current.

1. Calculate the maximum power dissipation, if not provided on the data sheet.
2. Using Equation 1, calculate the allowable ΔT_J rise above the elevated ambient temperature.
3. Calculate the required thermal resistance LED junction-to-ambient, $R_{\theta\text{J-A}}$.
4. Calculate the allowable thermal resistance pc board-to-ambient using Equation 2.

Using the above sample LED lamp, the following example calculations determine the thermal resistance requirements for operating at $T_A = 80^\circ\text{C}$ without dc current derating.

Step 1.

$$V_F (50 \text{ mA}) = 2.05 \text{ V.}$$

From Equation 3:

$$P_{\text{MAX}} = (0.050 \text{ A}) (2.05 \text{ V})$$

$$P_{\text{MAX}} = 0.103 \text{ W}$$

Step 2.

From Equation 1:

$$\Delta T_J = 110^\circ\text{C} - 80^\circ\text{C}$$

$$\Delta T_J = 30^\circ\text{C}$$

Step 3.

Using Equation 8:

$$R_{\theta\text{J-A}} = 30^\circ\text{C} / 0.103 \text{ W}$$

$$R_{\theta\text{J-A}} = 291^\circ\text{C/W}$$

Step 4.

From Equation 2:

$$R_{\theta\text{PC-A}} = 291^\circ\text{C/W} - 260^\circ\text{C/W}$$

$$R_{\theta\text{PC-A}} = 31^\circ\text{C/W}$$

To obtain this low a value for the pc board thermal resistance-to-ambient necessitates the use of a maximum metalized pc board, may require special heat sinking attached to the device leads, and forced air cooling. This means considerable cost is added to the design to allow for operation at 80°C without current derating.

www.hp.com/go/led_lamps

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